



LV-PECL or LVDS/ 3.3V or 2.5V/ 5.0×3.2mm



RoHS Compliant

**Features**

- High frequency to 800MHz
- LV-PECL output or LVDS output
- Miniature ceramic package
- Compact and low profile (5.0×3.2×1.2mm max.)
- Low current consumption

**Applications**

- WDM/ Networking

**Table 1**

Code	Freq. Tol. $\times 10^{-6}$	Operating Temperature Range (°C)	Note
G	$\pm 50$	-40 to +85	Please contact us for available frequencies.

**How to Order**

KV5032R 622.080 □ □ G D 00  
① ② ③ ④ ⑤ ⑥ ⑦

- ① Series
- ② Output Frequency
- ③ Output Type (P : LV-PECL or L : LVDS)
- ④ Supply Voltage (3 : 3.3V or 2 : 2.5V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ INH Function (45/ 55%, Disable)
- ⑦ Individual Specification (STD Specification is "00")

Packaging (Tape & Reel 1000 pcs./ reel)

**Specifications**

Item	Symbol	Conditions	Min.	Max.	Unit	
Output Frequency Range <sup>Note1</sup>	f <sub>o</sub>		10	800	MHz	
Frequency Tolerance	f <sub>tol</sub>	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration Temp.: -40 to +85°C	-50	+50	$\times 10^{-6}$	
Absolute Pull Range	APR		$\pm 100$	—	$\times 10^{-6}$	
Control Voltage	V <sub>c</sub>		0	+3.3	V	
Storage Temperature Range	T <sub>stg</sub>		-55	+125	°C	
Operating Temperature Range	T <sub>use</sub>		-40	+85	°C	
Max. Supply Voltage	—		-0.5	+4.2	V	
Supply Voltage	V <sub>cc</sub>		+2.25 +2.97	+2.75 +3.63	V	
Linearity	—	V <sub>c</sub> =0V to +3.3V	-10	10	%	
Current Consumption	I <sub>cc</sub>	LV-PECL Output (2.25≤V <sub>cc</sub> ≤2.75V)	—	80	mA	
		LV-PECL Output (2.97≤V <sub>cc</sub> ≤3.63V)	—	100		
		LVDS Output (2.25≤V <sub>cc</sub> ≤2.75V, 2.77≤V <sub>cc</sub> ≤3.63V)	—	40		
Symmetry	SYM	LV-PECL Output 50ohm @crossing point	45	55	%	
		LVDS Output 100ohm @crossing point	45	55		
Rise/ Fall Time (20% to 80% Output Level)	Tr/ Tf	LV-PECL Output 50ohm LVDS Output 100ohm	—	0.4 0.6	ns	
Low Level Output Voltage <sup>Note2</sup>	V <sub>OL</sub>	LV-PECL Output	—	V <sub>cc</sub> - 1.620	V	
High Level Output Voltage <sup>Note2</sup>	V <sub>OH</sub>		V <sub>cc</sub> - 1.025	—	V	
Output Load	—		—	50	ohm	
Low Level Output Voltage <sup>Note2</sup>	V <sub>OL</sub>	LVDS Output	Typ. 1.1V	0.9	V	
High Level Output Voltage <sup>Note2</sup>	V <sub>OH</sub>		Typ. 1.43V	—	1.6	V
Differential Output Voltage <sup>Note2</sup>	V <sub>OD</sub>		Typ. 330mV	175	454	mV
Differential Output Voltage Error <sup>Note2</sup>	dV <sub>OD</sub>		dV <sub>OD</sub> =  V <sub>OD1</sub> - V <sub>OD2</sub>	—	50	mV
Offset Voltage	V <sub>OS</sub>		Typ. 1.25V	1.125	1.375	V
Offset Voltage Error	dV <sub>OS</sub>		dV <sub>OS</sub> =  V <sub>OS1</sub> - V <sub>OS2</sub>	—	50	mV
Output Load	—		—	100	ohm	
Low Level Input Voltage <sup>Note2</sup>	V <sub>IL</sub>		—	30% V <sub>cc</sub>	V	
High Level Input Voltage <sup>Note2</sup>	V <sub>IH</sub>		70% V <sub>cc</sub>	—	V	
Input Resistance	—		150	—	k ohm	
Disable Time	t <sub>dis</sub>		—	200	ns	
Enable Time	t <sub>ena</sub>		—	2	ms	
Start-up Time	t <sub>str</sub>	@Minimum operating voltage to be 0 sec.	—	10	ms	
Phase Jitter	J <sub>phase</sub>	@622.08MHz	BW : 12kHz to 20MHz @10Hz offset @100Hz offset @1kHz offset	Typ. 3.0 Typ. -40 Typ. -70 Typ. -95	dBc/ Hz	
Phase Noise	—	@622.08MHz	@10kHz offset	Typ. -105		
			@100kHz offset	Typ. -105		
			@1MHz offset	Typ. -125		
			@10MHz offset	Typ. -135		

Note : All electrical characteristics are defined at the maximum load and operating temperature range.  
Note1: Please contact us for inquiry about operating temperature range, available frequencies and other conditions. Note2: DC characteristic

**Dimensions**

(Unit: mm)

**Recommended Land Pattern**

(Unit: mm)

