



LV-PECL/ 3.3V/ 7.0×5.0mm



RoHS Compliant

Features

- Miniature ceramic package
- LV-PECL output
- High frequency to 170MHz
- Low Phase Noise

Table 1

Freq. Tol. Code × 10 ⁻⁶	Operating Temperature Range (°C)	Note
0	±50	-10 to +70 Standard specifications
S	±30	Please contact us for available frequencies.
G	±50	-40 to +85

How to Order

KV7050C 155.520 P 3 □ J 00

① ② ③ ④ ⑤ ⑥ ⑦

①Series

②Output Frequency

③Output Type (LV-PECL)

④Supply Voltage (3.3V)

⑤Frequency Tolerance (See Table 1)

⑥Symmetry/ INH Function/ APR
(45/ 55%, Disable, APR50)

⑦Individual Specification
(STD Specification is "00".)

Packaging (Tape & Reel 1000 pcs./ reel)

Specifications

Item	Symbol	Conditions	Min.	Max.	Unit
Output Frequency Range ^{Note1}	fo		80	170	MHz
Frequency Tolerance ^{Note2}	f _{tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Shock and vibration	-50	+50	×10 ⁻⁶
		Temp.: -10 to +70°C/ -40 to +85°C	-30	+30	
Absolute Pull Range	APR	Standard Specifications	±50	—	×10 ⁻⁶
		Extend (Option)	±100	—	
Frequency Aging	f _{age}	Per 20years @25°C	-15	+15	×10 ⁻⁶
Control Voltage	V _c		0	+3.3	V
Storage Temperature Range	T _{stg}		-55	+125	°C
Operating Temperature Range	T _{use}	Standard Specifications Extend (Option)	0	+70	°C
Max. Supply Voltage	—		-0.3	+5.0	V
Supply Voltage	V _{cc}		+2.97	+3.63	V
Current Consumption	I _{cc}		—	85	mA
Disable Current	I _{dis}		—	5	mA
Symmetry	SYM	50ohm@crossing point	45	55	%
Rise/ Fall Time (20% to 80% Output Level)	Tr/ Tf	50ohm	—	0.7	ns
Low Level Output Voltage ^{Note3}	V _{OL}		—	V _{cc} -1.620	V
High Level Output Voltage ^{Note3}	V _{OH}		V _{cc} -1.025	—	V
Output Load	—	LV-PECL Output	50	—	ohm
Input Voltage Range	V _{IN}		0	+3.3	V
Low Level Input Voltage ^{Note3}	V _{IL}		—	30% V _{cc}	V
High Level Input Voltage ^{Note3}	V _{IH}		70% V _{cc}	—	V
Input Resistance	—		10	—	Mohm
Disable Time	t _{dis}		—	200	ns
Enable Time	t _{ena}		—	2	ms
Start-up Time	t _{str}	@Minimum operating voltage to be 0 sec.	—	10	ms
Phase Jitter	J _{Phase}	@122.88MHz	BW : 12kHz to 20MHz	—	ps
Phase Noise	—	@122.88MHz	@10Hz offset	Typ. -71	dBc/ Hz
			@100Hz offset	Typ. -102	
			@1kHz offset	Typ. -128	
			@10kHz offset	Typ. -146	
			@100kHz offset	Typ. -152	
			@1MHz offset	Typ. -156	
			@10MHz offset	Typ. -157	

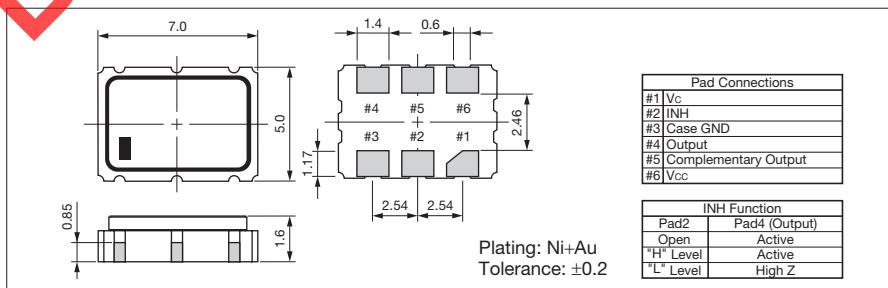
Note : All electrical characteristics are defined at the maximum load and operating temperature range.

Note1: Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Note2: Please contact us for the Frequency tolerance of -40 to +85°C.

Note3: DC characteristic

Dimensions



Recommended Land Pattern

