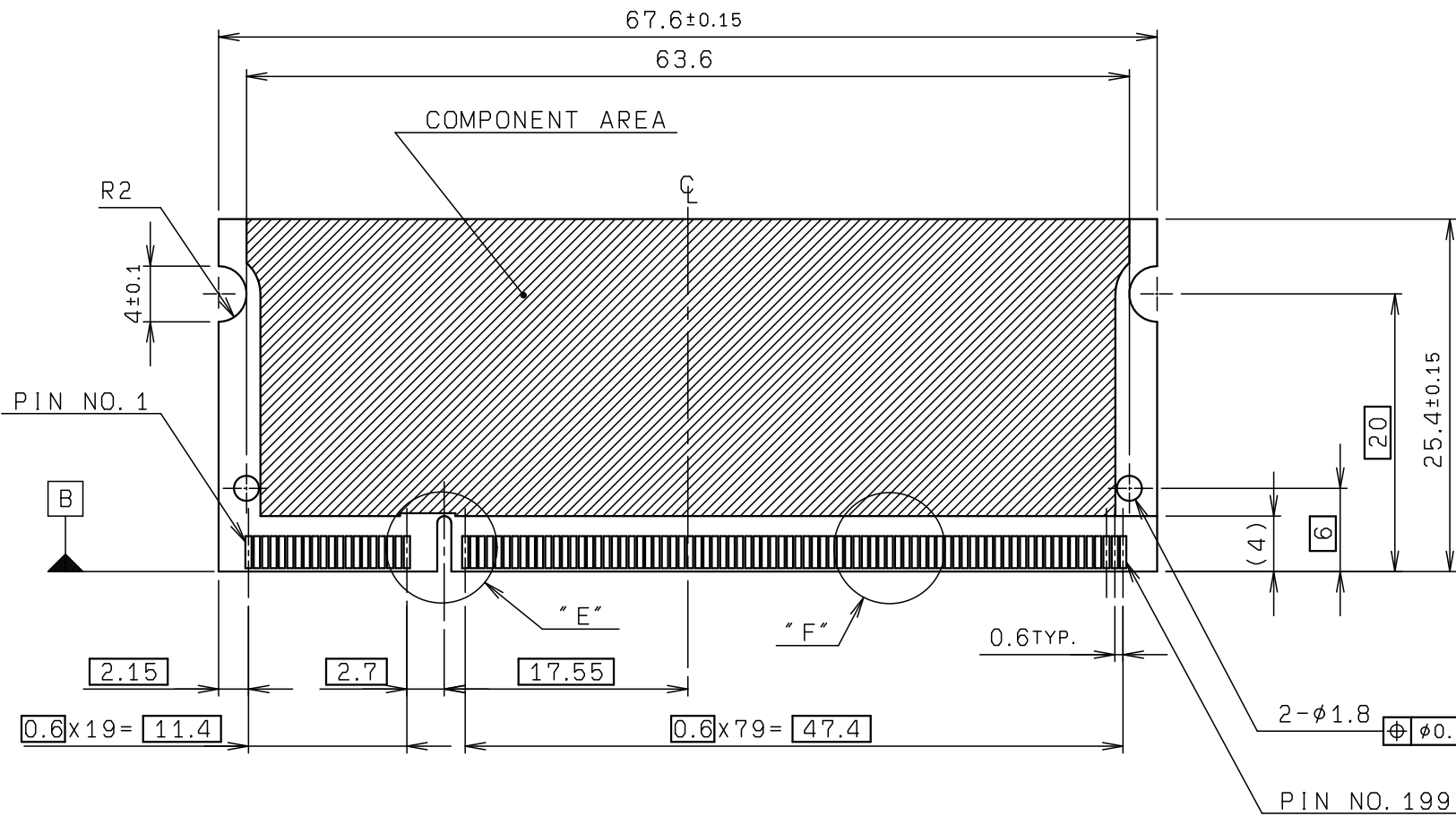
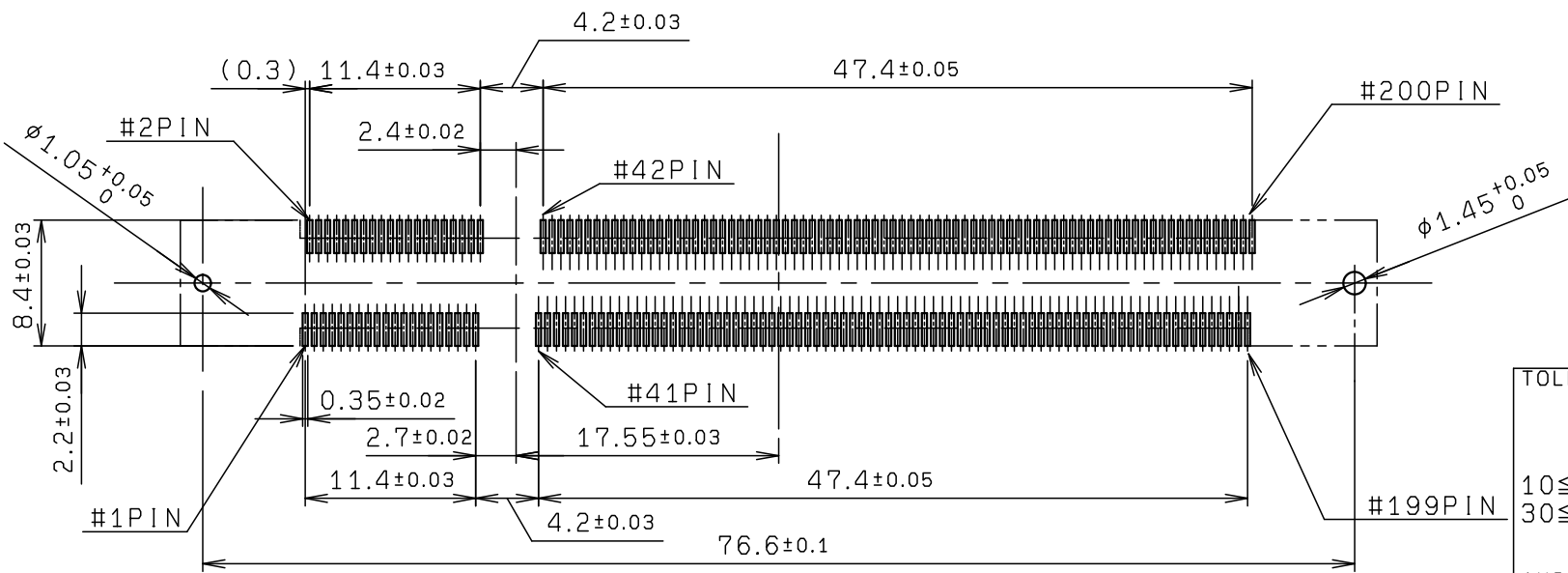
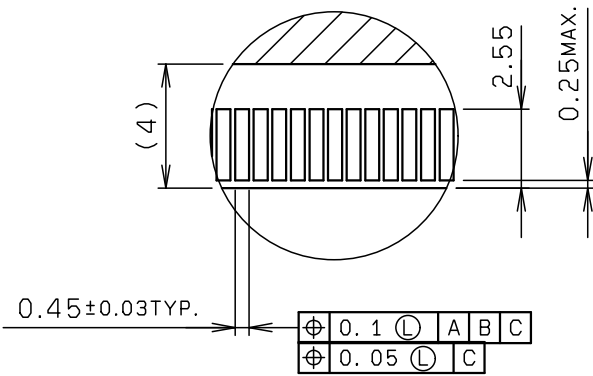
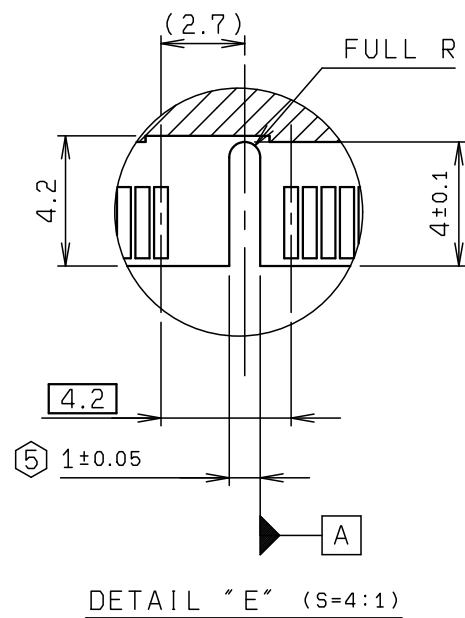
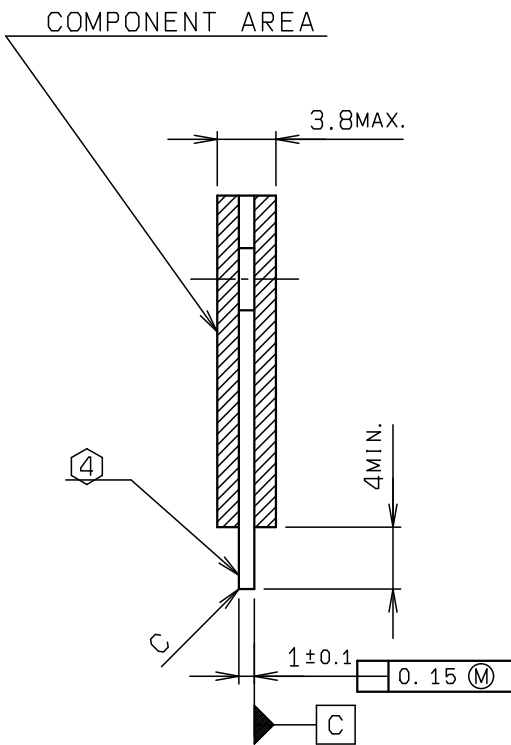


NO.	DESCRIPTION	PART NO.	QTY	MATERIAL	DRW NO.	REMARKS
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推奨メモリ基板寸法
RECOMMENDED MEMORY BOARD LAYOUT



基板取付寸法
MOUNTING LAYOUT

ROHS規制対応
COMPLIANT WITH
ROHS REGULATIONS

TOLERANCE / 一般公差 (S)		PRODUCT SPEC / 製品規格							
X < 10 ±0.2		UNIT mm		ANGLE 3RD		REFER TO SHEET 1/2			
10 ≤ X < 30 ±0.25		SCALE 2 : 1		DATE 2005/12/06		NO.	EDN/DCN	DATE	ISSUE
30 ≤ X ±0.3		ANGLE / 角度 ±3°		DRAWN LI		PART NAME / 品名			
DESIGNED M. YOSHIDA		CHECKED Y. SHIROYAMA		APPROVED N. HAYASHI		SERIES 6406 REC ASSY ST SMT			
KYOCERA Corporation		DWG NO. / 図番		SHEET / REV		PART NO. / 型番			
KYOCERA Corporation		BJS 6406012		12/2 F		DDR_S.O.-DIMM (1.8V) 200p WITH VACUUM TAPE			
						20 6406 200 101 856 +			

