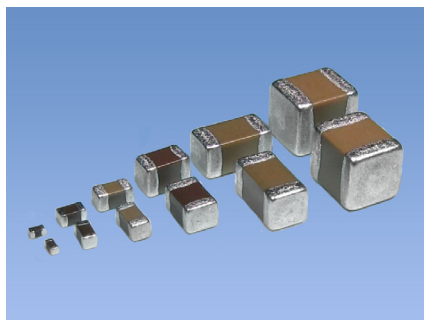


How to Order



■ Features

- Kyocera's series of Multilayer Ceramic Chip Capacitors are designed to meet a wide variety of needs. We offer a complete range of products for both general and specialized applications.
- We have a network worldwide in order to supply our global customer bases quickly and efficiently.
- All our products are highly reliable due to their monolithic structure of high-purity and superfine uniform ceramics and their integral internal electrodes.
- Our stringent quality control in every phase of production from material procurement to shipping ensures consistent manufacturing and superior quality.
- Kyocera components are available in a wide choice of dimensions, temperature characteristics, rated voltages, and terminations to meet specific configurational requirements.

■ KYOCERA PART NUMBER

CM 03 X5R 225 M 06 A H □□□
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ OPTION :

Above digits are used to track individual specification or thickness.

(Example)

- ① Series : CM Series (General)
- ② Size : 0201
- ③ Dielectric : X5R
- ④ Capacitance : 2.2μF
- ⑤ Tolerance : ±20%
- ⑥ Voltage : 6.3Vdc
- ⑦ Termination : Sn
- ⑧ Packaging : Cavity pitch 2mm / Reel Size φ180

① Series Code

CODE	Type
CM	General
CT	Low Profile
CU	High-Q
AR	Automotive
KNH	Three Terminal Capacitors

② Size Code

CODE	EIA	JIS
02	01005	0402
03	0201	0603
05	0402	1005
105	0603	1608
21	0805	2012
316	1206	3216
32	1210	3225

③ Dielectric Code

Temperature Compensation Type			
CODE	Temperature Range (°C)	ppm/°C	
CG	-55 to 125	0	±30
CH			±60

- All parts of COG will be marked as "CG" but will conform to the above table.
- Temperature coefficients are determined by calculation based on measurement at 20°C and 85°C.

High Dielectric Constant Type			
CODE	Temperature Range (°C)	ΔC (%)	Standard Temperature (°C)
X5R	-55 to 85	±15	25
X6S	-55 to 105	±22	
X6T		+22/-33	
X7R	-55 to 125	±15	
X7S		±22	
X7T		+22/-33	

④ Capacitance Code

Capacitance expressed in pF.
 Two significant digits plus number of zeros.
 For Values < 10pF, Letter R denotes decimal point,
 102=1,000pF=1nF

(Example)

CODE	Capacitance
R50	0.5pF
1R0	1pF
100	10pF
101	100pF
102	1nF
103	10nF
104	100nF
105	1μF
106	10μF
107	100μF

⑤ Tolerance Code

Temperature Compensation Type (COG)	
CODE	Tolerance
A*	±0.05pF
B	±0.1pF
C	±0.25pF
D	±0.5pF
G*	±2%
J	±5%
K	±10%

* : Option

⑥ Voltage Code

CODE	Rated Voltage
02	2.5Vdc
04	4Vdc
06	6.3Vdc
10	10Vdc
16	16Vdc
25	25Vdc
35	35Vdc
50	50Vdc
100	100Vdc

⑦ Termination Code

CODE	Termination
A	Nickel Barrier/ Tin

- Please contact us if Au termination is needed.

⑧ Packaging Code

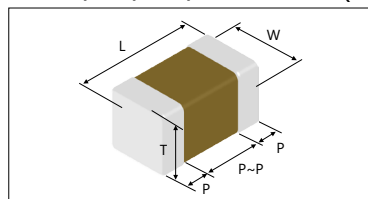
CODE	Size Code	Cavity pitch	Reel size
T	105 to 32	4mm	φ180
H	02 to 05	2mm	
Q	03/05	1mm	
P	02	1mm	φ330
L	105 to 32	4mm	
N	02 to 05	2mm	
W	03/05	1mm	

High Dielectric Constant Type (X5R/X6S/X6T/X7R/X7S/X7T)	
CODE	Tolerance
J*	±5%
K	±10%
M	±20%

* : Option

Dimension

■CM/CT/CU/AR Series (Two Terminal Capacitors)



■Packaging Code

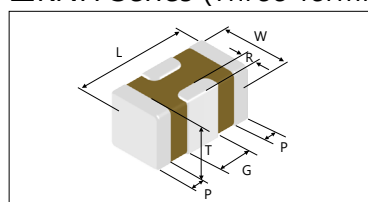
20kp (E 8 / 2)

kp means 1000 pieces

Taping Material		Taping Width		Pitch	
Code	Material	Code	Width	Code	Pitch
E	Plastic	4	4mm	1	1mm
P	Paper	8	8mm	2	2mm
				4	4mm

Size	Code		Dimension Code	Dimension (mm)						Quantity per reel	
	EIA	JIS		L	W	T	P min.	P max.	P to P min.	φ180 Reel	φ330 Reel
02	01005	0402	A	0.4±0.02	0.2±0.02	0.2±0.02	0.07	0.14	0.13	40kp(E4/1) 20kp(P8/2)	—
03	0201	0603	A	0.6±0.03	0.3±0.03	0.22 max.	0.1	0.2	0.2	30kp(P8/1) 15kp(P8/2)	150kp(P8/1) 50kp(P8/2)
			B			0.3±0.03					
			C	0.6±0.05	0.3±0.05	0.3±0.05					
			D			0.22 max.					
			E	0.6±0.09	0.3±0.09	0.3±0.09	0.13	0.23	0.19	15kp(P8/2)	50kp(P8/2)
			F			0.5±0.05				10kp(P8/2)	—
05	0402	1005	A	1.0±0.05	0.5±0.05	0.33 max.				20kp(P8/1) 10kp(P8/2)	100kp(P8/1) 50kp(P8/2)
			B			0.5±0.05					
			C	1.0±0.1	0.5±0.05	0.22 max.				10kp(P8/2)	50kp(P8/2)
			D	1.0±0.15	0.5±0.15	0.5±0.15				10kp(P8/2)	40kp(P8/2)
			E			0.33 max.	0.15	0.35	0.3	10kp(P8/2)	—
			F			0.5 max.				10kp(P8/2)	50kp(P8/2)
			G	1.0±0.2	0.5±0.2	0.55 max.				10kp(P8/2)	40kp(P8/2)
			H			0.5±0.2				10kp(P8/2)	30kp(P8/2)
			J			0.8 max.					
			A	1.6±0.1	0.8±0.1	0.55 max.					
105	0603	1608	B			0.8±0.1	0.2	0.6	0.5	4kp(P8/4)	10kp(P8/4)
			C	1.6±0.15	0.8±0.15	0.8±0.15					
			D	1.6±0.2	0.8±0.2	0.8±0.2					
			A	2.0±0.1	1.25±0.1	1.25±0.1				3kp(E8/4)	10kp(E8/4)
21	0805	2012	B	2.0±0.15	1.25±0.15	0.95 max.	0.2	0.75	0.7	4kp(P8/4)	10kp(P8/4)
			C			0.95 max.					
			D	2.0±0.2	1.25±0.2	1.25±0.2				3kp(E8/4)	10kp(E8/4)
			A	3.2±0.2	1.6±0.2	0.95 max.				4kp(P8/4)	—
316	1206	3216	B	3.2±0.2	1.6±0.15	1.6±0.15	0.3	0.85	1.4	2.5kp(E8/4)	5kp(E8/4)
			C		1.6±0.2	1.6±0.2					
			D	3.2±0.3	1.6±0.3	1.6±0.3	0.3	0.85	1.9	2kp(E8/4)	—
			A	3.2±0.3	2.5±0.2	2.5±0.2	0.3	1.0	1.4	1kp(E8/4)	4kp(E8/4)

■KNH Series (Three Terminal Capacitors)



Size	Code		Dimension Code	Dimension (mm)						Packaging	
	EIA	JIS		L	W	T	G	P	R	φ180 Reel	φ330 Reel
KNH 05	0402	1005	A	1.0±0.1	0.5±0.2	0.5 max.					
			B	1.0±0.15	0.5±0.15	0.5±0.15	0.3±0.1	0.15±0.1	≥0.05	10kp(P8/2)	—
			C	1.0±0.2	0.5±0.2	0.5±0.2					

■Features

With our unique materials and manufacturing technology, we provide products that fully bring out the performance of equipment even in the highly reliable environment required for automotive application.

■Applications

●ECU,ADAS,ESC,ABS,LCD panel

X7R/X7S Dielectric

●Capacitance chart Standard Spec.3

	X7R	X7S
Size (EIA Code)	AR05 (0402)	AR21 (0805)
Rated Voltage (Vdc)	6.3	10
Capacitance		
104 0.1 μF		
224 0.22 μF		
474 0.47 μF		
105 1 μF		
225 2.2 μF		
475 4.7 μF		
106 10 μF		

Please contact for capacitance value other than standard.

Please refer to [here](#) for the test method and specifications of Standard Specification 3.

Two digits alphanumerics in capacitance chart denote dimensions and tan δ. Please refer to the above table for detail.

(Example) In case of "C5" for AR05;

L: 1.0±0.05mm, W: 0.5±0.05mm, T: 0.5±0.05mm, Tanδ: 7.5% max.

Tan δ Code	Tan δ
5	7.5% max.
7	10.0% max.

Size	Dimension Code	Dimension (mm)			Packaging									
					φ180 Reel					φ330 Reel				
		L	W	T	Code	Quantity (pcs.)	Taping Material	Taping Width (mm)	Cavity Pitch (mm)	Code	Quantity (pcs.)	Taping Material	Taping Width (mm)	Cavity Pitch (mm)
05	B	1.0±0.05	0.5±0.05	0.5±0.05	H	10,000	Paper	8	2	N	50,000	Paper	8	2
21	D	2.0±0.2	1.25±0.2	1.25±0.2	T	3,000	Plastic	8	4	L	10,000	Plastic	8	4

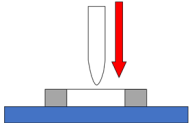


Test Conditions and Specifications for High Dielectric Type (X7R, X7S) AR Series (Standard Spec.3)

Test Items		Test Conditions (Complies with AEC-Q200)	Specifications
High Temperature Exposure (Storage)	Appearance	Take the initial value after heat treatment.	No problem observed
	Capacitance Variation	Temperature : 150±3°C Time : 1000±12h	Within±12.5%
	Tanδ	MIL-STD-202 Method 108 Measure after heat treatment.	Within tolerance
	IR	The charge and discharge current of the capacitor must not exceed 50mA.	Over 50MΩ•μF
Temperature Cycle	Appearance	Take the initial value after heat treatment.	No problem observed
	Capacitance Variation	1cycle : refer to the table on the right. Number of cycles : 1,000cycles JESD22 Method JA-104	Within±10.0%
	Tanδ	Measurement after heat treatment.	Within tolerance
	IR	The charge and discharge current of the capacitor must not exceed 50mA.	Over 50MΩ•μF
Moisture Resistant Load	Appearance	Take the initial value after heat treatment.	No problem observed
	Capacitance Variation	Temperature : 85±3°C Humidity : 80~85%RH Voltage : Rated voltage Time : 1000±12h	Within±12.5%
	Tanδ	MIL-STD-202 Method 103 Measurement after heat treatment.	200% max. of initial value
	IR	The charge and discharge current of the capacitor must not exceed 50mA.	Over 10MΩ • μF
High-Temperature Load	Appearance	Take the initial value after heat treatment.	No problem observed
	Capacitance Variation	Temperature : 125±3°C Voltage : Rated voltage Time : 1000±12h	Within±12.5%
	Tanδ	MIL-STD-202 Method 108 Measurement after heat treatment.	200% max. of initial value
	IR	The charge and discharge current of the capacitor must not exceed 50mA.	Over 10MΩ • μF
Appearance		External Visual MIL-STD-883 Method 2009	No problem observed
Dimensions		Physical Dimensions JESD22 Method JB-100	Refer to capacitance chart
Resistance to Solvent	Appearance	Take the initial value after heat treatment.	No problem observed
	Capacitance	The mixing ratio is the volume ratio Solvent 1 : Solvent of mineral spirits 3 to isopropyl alcohol 1. Solvent 2 : Terpene defluxer	Within tolerance
	Tanδ	Solvent 3 : 42 parts (by volume) of water 1 part (by volume) of propylene glycol monomethyl ether 1 part (by volume) of monoethanolamine.	Within tolerance
	IR	MIL-STD-202 Method 215 Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.	Over 50MΩ•μF
Mechanical Shock	Appearance	Take the initial value after heat treatment.	No problem observed
	Capacitance	Pulse : half sine shock pulse Time : 0.5ms Maximum shock : 1500G Speed : 4.7m/s	Within tolerance
	Tanδ	Shock times : Three shocks in each direction shall be applied along the three mutually perpendicular axes of the test specimen (18 shocks). MIL-STD-202 Method 213	Within tolerance
	IR	Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.	Over 50MΩ•μF
Vibration Test	Appearance	Take the initial value after heat treatment.	No problem observed
	Capacitance	Vibration frequency : 10Hz~2000Hz~10Hz(20 min.) Acceleration : 5.0g's	Within tolerance
	Tanδ	Sweep time and duration : This cycle shall be performed 12 times in each of three mutually perpendicular directions(total of 36 times). MIL-STD-202 Method 204	Within tolerance
	IR	Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.	Over 50MΩ•μF



Test Conditions and Specifications for High Dielectric Type (X7R, X7S) AR Series (Standard Spec.3)

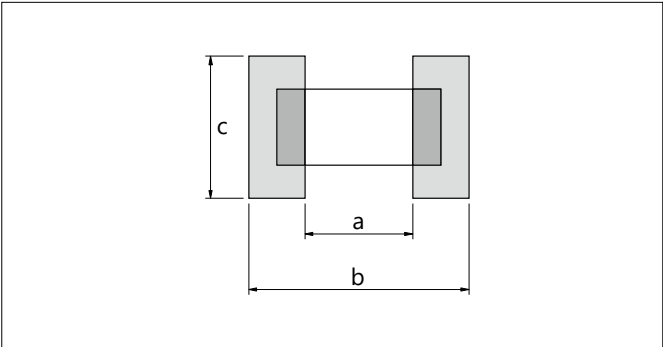
Test Items		Test Conditions (Complies with AEC-Q200)	Specifications									
Soldering Heat Resistant	Appearance	Take the initial value after heat treatment. Solder melting temperature : 260+/-5℃ Dipping time : 10+/-1sec MIL-STD-202 Method 210 Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.	No problem observed									
	Capacitance Variation		Within±10.0%									
	Tanδ		Within tolerance									
	IR		Over 50MΩ•μF									
ESD	Appearance	Take the initial value after heat treatment. AEC-Q200-002 Voltage level : 2KV Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.	No problem observed									
	Capacitance		Within tolerance									
	Tanδ		Within tolerance									
	IR		Over 50MΩ•μF									
Solderability	Appearance	Pretreatment : 155+0/-10℃ left for 4h+/-10minutes Flux : Rosin-Ethanol(25wt%) Solder composition : Sn-3.0Ag-0.5Cu(SAC305) Solder melting temperature : 245+/-5℃ Dipping time : 5+/-0.5sec	Solder coverage : 95% min.									
Electrical Characterization	Capacitance	Measure after heat treatment. Measuring frequency : 1kHz±10% Measuring voltage : 1.0±0.2Vrms	Within tolerance									
	Tanδ		Refer to capacitance chart									
	IR	At room temperature and humidity, the rated voltage is applied for one minute and then measured. The charge and discharge current of the capacitor must not exceed 50mA.	Over 50MΩ•μF									
	Withstanding Voltage	Apply 2.5 times the rated voltage for 1~5 sec. The charge and discharge current of the capacitor must not exceed 50mA.	Resist without problem									
Bending Strength	Appearance	Take the initial value after heat treatment. Pressing speed : 1.0mm/s Flexible volume : 2mm Pressing time : 60+5/-0 sec. AEC-Q200-005 The charge and discharge current of the capacitor must not exceed 50mA.	No problem observed									
	Capacitance Variation		Within±10.0%									
	Tanδ		Within tolerance									
	IR		Over 50MΩ•μF									
Termination Strength	Appearance	Take the initial value after heat treatment. Pressing force : AR05 : 2N AR21 : 18N Pressing time : 60+/-1sec AEC-Q200-006 The charge and discharge current of the capacitor must not exceed 50mA.	No problem observed									
	Capacitance		Within tolerance									
	Tanδ		Within tolerance									
	IR		Over 50MΩ•μF									
Beam Load	Breaking strength		The Breaking strength exceeds the following load. AR21 : 20N AR05 : 8N									
	Appearance		No significant damage with the following loads AR21 : 20N AR05 : 8N									
Temperature characteristics	Capacitance Variation	Temperature range : -55~+125℃ Reference temperature : 25℃ Measuring condition: <table><tr><td></td><td>AR05</td><td>AR21</td></tr><tr><td>Measuring frequency</td><td>1kHz±10%</td><td>1kHz±10%</td></tr><tr><td>Measuring voltage</td><td>0.1±0.02Vrms</td><td>1.0±0.2Vrms</td></tr></table>		AR05	AR21	Measuring frequency	1kHz±10%	1kHz±10%	Measuring voltage	0.1±0.02Vrms	1.0±0.2Vrms	X7R : Within±15% X7S : Within±22%
	AR05	AR21										
Measuring frequency	1kHz±10%	1kHz±10%										
Measuring voltage	0.1±0.02Vrms	1.0±0.2Vrms										
Heat treatment		Expose sample in the temperature of 150+0/ - 10℃ for 1 hour and leave the sample in normal temperature and humidity for 24±2 hours.										



Test Conditions and Standards

Substrate for Adhesion Strength Test, Vibration Test, Soldering Heat Resistance Test, Temperature Cycle Test, Load Humidity Test, High-Temperature with Loading Test.

(Unit: mm)



Size (EIA Code)	a	b	c
02 (01005)	0.15	0.5	0.2
03 (0201)	0.26	0.92	0.32
05 (0402)	0.4	1.4	0.5
105 (0603)	1.0	3.0	1.2
21 (0805)	1.2	4.0	1.65
316 (1206)	2.2	5.0	2.0
32 (1210)	2.2	5.0	2.9

Substrate for Bending Test

(Unit: mm)

Testing Board : Glass Epoxy Board (GE4 or FR4)
Testing Board Thickness : 1.6±0.2*
Circuit Thickness : 0.04±0.01

*02, 03, 05 size 0.8±0.1mm

Structure

Plated Termination

Internal Electrodes(Ni or Cu)

Dielectric Ceramic Layer
Temperature compensation:
Zirconate family
High dielectric constant:
Barium Titanate family

External Termination Electrodes (Cu or CuNi)

Ni Plating

Sn Plating

■Certification status

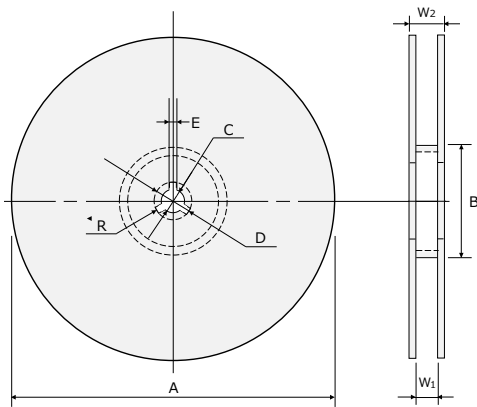
- <ISO>
Acquired ISO 9001 quality management system certification.
- <IATF>
Acquired IATF 16949 quality management system certification.

■Production plant

Kagoshima kokubu plant

Packaging Options Tape and Reel

Reel



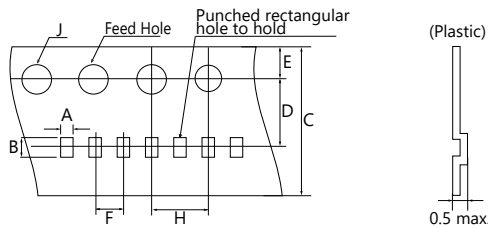
(Unit: mm)

Code Reel	A	B	C	D
7-inch Reel (CODE: T, H, Q)	180 ⁺⁰ _{-2.0}	φ60 min.	13±0.5	21±0.8
7-inch Reel (CODE: P)	178±2.0			
13-inch Reel (CODE: L, N, W)	330±2.0			
Code Reel	E	W ₁	W ₂	R
7-inch Reel (CODE: T, H, Q)	2.0±0.5	10.5±1.5	16.5 max.	1.0
7-inch Reel (CODE: P)		4.35±0.3	6.95±1.0	
13-inch Reel (CODE: L, N, W)		9.5±1.0	16.5 max.	

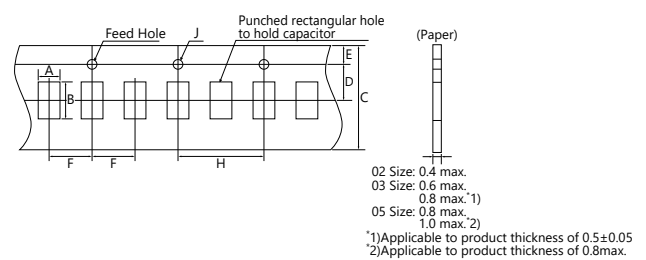
Carrier Tape

(Unit: mm)

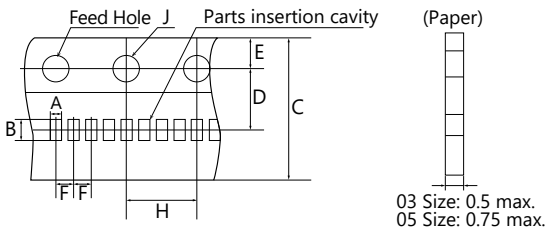
F=1mm (02 Size)



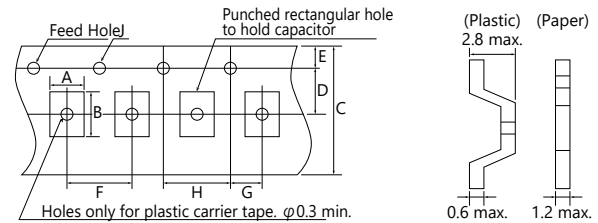
F=2mm (02, 03, 05 Size)



F=1mm (03, 05 Size)



F=4mm (105, 21, 316, 32 Size)



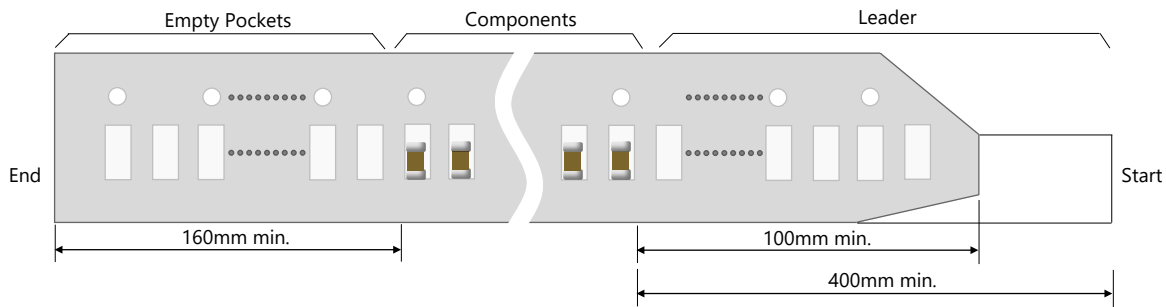
Size (EIA Code)	A	B	C	D	E	F	G	H	J	Carrier Tape	
										Width	Material
02 (01005)*	0.24±0.02	0.44±0.02	4.0±0.08	1.8±0.02	0.9±0.05	1.0±0.02	—	2.0±0.04	0.8±0.04	4	Plastic
	0.25±0.03	0.45±0.03	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		4.0±0.1	1.5±0.1/-0	8	Paper
03 (0201)*	0.37±0.03	0.67±0.03	8.0±0.3/-0.1	3.5±0.05	1.75±0.1	1.0±0.05	—	4.0±0.05	1.5±0.1/-0	8	Paper
	0.39±0.03	0.69±0.03	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		4.0±0.1	1.5±0.1/-0		
	0.42±0.03	0.72±0.03	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		4.0±0.1	1.5±0.1/-0		
	0.44±0.05	0.74±0.05	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		4.0±0.1	1.5±0.1/-0		
05 (0402)*	0.65±0.1	1.15±0.1	8.0±0.3/-0.1	3.5±0.05	1.75±0.1	1.0±0.05	—	4.0±0.05	1.5±0.1/-0	8	Paper
	0.75±0.1		8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		4.0±0.1	1.5±0.1/-0		
	0.8±0.1		8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		4.0±0.1	1.5±0.1/-0		
105 (0603)*	1.0±0.2	1.8±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5±0.1/-0	8	Paper
	1.1±0.2	1.9±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5±0.1/-0		
21 (0805)	1.5±0.2	2.3±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5±0.1/-0	8	Paper
316 (1206)	2.0±0.2	3.6±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5±0.1/-0	8	Paper
32 (1210)	2.9±0.2	3.6±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5±0.1/-0	8	Plastic

* Option



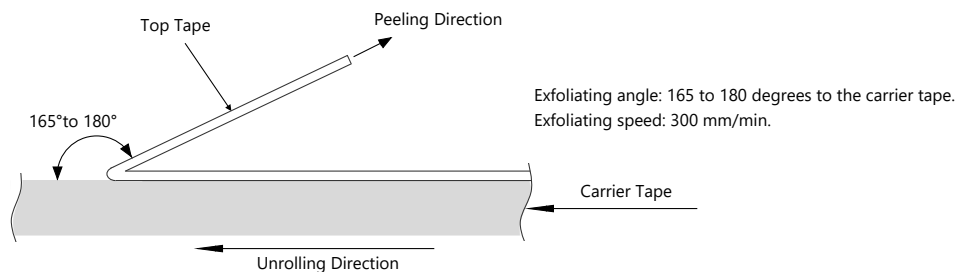
Packaging Options

Detail of leader and trailer



Adhesive tape

- 1) The exfoliative strength when peeling off the top tape from the carrier tape by the method of the following figure shall be *0.1 to 0.7N. *02 Size: 0.1 to 0.5N
- 2) When the top tape is peeled off, the adhesive stays on the top tape.
- 3) Chip capacitors will be in a state free without being stuck on the thermal adhesive tape.



Carrier tape

- 1) Chip will not fall off from carrier tape or carrier tape will not be damaged by bending than within a radius of 25mm.
- 2) The chip are inserted continuously without any empty pocket.
- 3) Chip will not be mis-mounted because of too big clearance between components and cavity. Also the waste of carrier tape will not fill a nozzle hole of mounting machine.

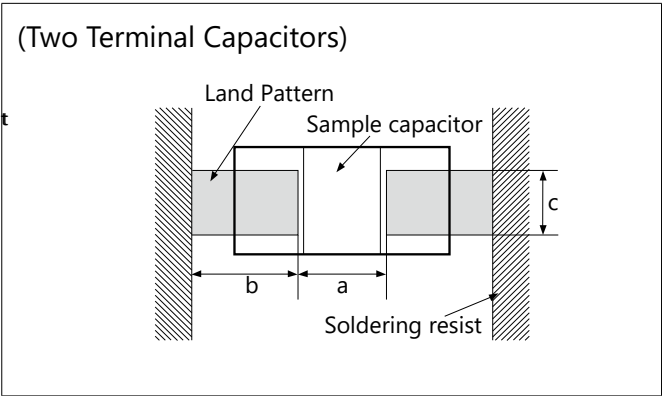


Surface Mounting Information

Dimensions for recommended typical land

Since the amount of solder (size of fillet) to be used has direct influence on the capacitor after mounting, the sufficient consideration is necessary.

When the amounts of solder is too much, the stress that a capacitor receives becomes larger. It may become the cause of a crack in the capacitor. When the land design of printed wiring board is considered, it is necessary to set up the form and size of land pattern so that the amount of solder is suitable.



Two Terminal Capacitors

(Unit: mm)

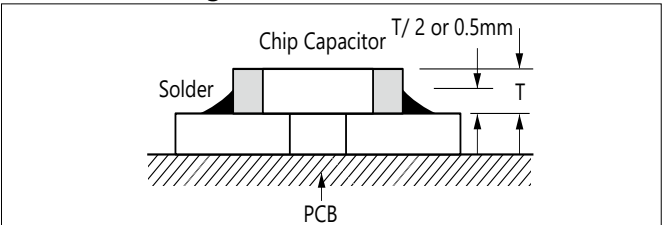
Size (EIA Code)	Dimension		Recommended land dimensions		
	L	W	a	b	c
02 (01005)	0.4±0.02	0.2±0.02	0.13 to 0.2	0.12 to 0.18	0.2 to 0.23
	0.6±0.03	0.3±0.03	0.2 to 0.25	0.25 to 0.35	0.3 to 0.4
03 (0201)	0.6±0.05	0.3±0.05	0.23 to 0.3	0.25 to 0.35	0.3 to 0.45
	0.6±0.09	0.3±0.09	0.3 to 0.5	0.35 to 0.45	0.4 to 0.6
05 (0402)	1.0±0.05	0.5±0.05	0.4 to 0.6	0.4 to 0.5	0.5 to 0.75
	1.0±0.15	0.5±0.15	0.7 to 1.0	0.8 to 1.0	0.6 to 0.9
105 (0603)	1.6±0.1	0.8±0.1	0.8 to 1.0	0.8 to 1.0	0.8 to 1.1
	1.6±0.15	0.8±0.15	1.0 to 1.3	1.0 to 1.2	1.0 to 1.45
21 (0805)	1.6±0.2	0.8±0.2	1.0 to 1.3	1.0 to 1.2	1.25 to 1.55
	1.6±0.25	0.8±0.25	2.1 to 2.5	1.1 to 1.3	1.4 to 1.9
316 (1206)	2.0±0.1	1.25±0.1	2.1 to 2.5	1.1 to 1.3	1.6 to 2.0
	2.0±0.15	1.25±0.15	2.1 to 2.5	1.1 to 1.3	1.9 to 2.8
32 (1210)	2.0±0.2	1.25±0.2			
	3.2±0.2	1.6±0.15			
	3.2±0.2	1.6±0.2			
	3.2±0.3	1.6±0.3			
	3.2±0.3	2.5±0.2			

* Recommended land dimensions may differ depending on dimensional tolerance.

Design of printed circuit and Soldering

The recommended fillet height shall be 1/2 of the thickness of capacitors or 0.5mm. When mounting two or more capacitors in the common land, it is necessary to separate the land with the solder resist strike so that it may become the exclusive land of each capacitor.

Ideal Solder Height



Item	Prohibited	Recommended example : Separation by solder resist
Multiple parts mount		
Mount with leaded parts		
Wire soldering after mounting		
Side by side layout		

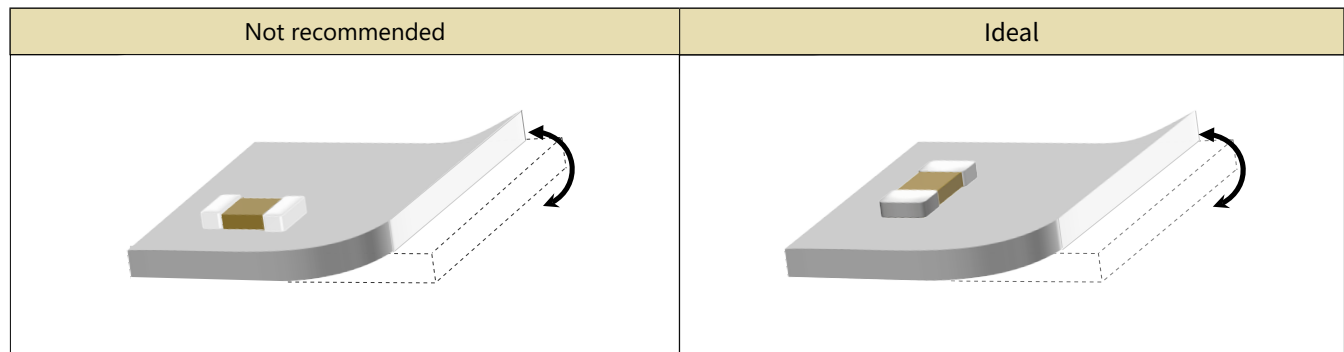


Surface Mounting Information

Mounting Design

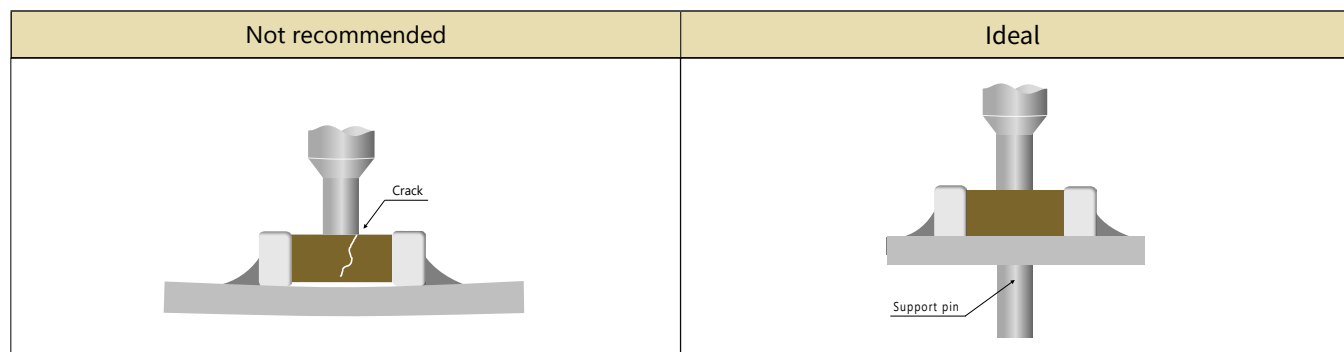
The chip could crack if the PCB warps during processing after the chip has been soldered.

Recommended chip position on PCB to minimize stress from PCB warpage



Actual Mounting

- 1) If the position of the vacuum nozzle is too low, a large force may be applied to the chip capacitor during mounting, resulting in cracking.
- 2) During mounting, set the nozzle pressure to a static load of 1 to 3 N.
- 3) To minimize the shock of the vacuum nozzle, provide a support pin on the back of the PCB to minimize PCB flexure.
- 4) Bottom position of pick up nozzle should be adjusted to the top surface of a substrate which camber is corrected.



Resin Mold

- 1) If a large amount of resin is used for molding the chip, cracks may occur due to contraction stress during curing. To avoid such cracks, use a low shrinkage resin.
- 2) The insulation resistance of the chip will degrade due to moisture absorption. Use a low moisture absorption resin.
- 3) Check carefully that the resin does not generate a decomposition gas or reaction gas during the curing process or during normal storage. Such gases may crack the chip capacitor or damage the device itself.



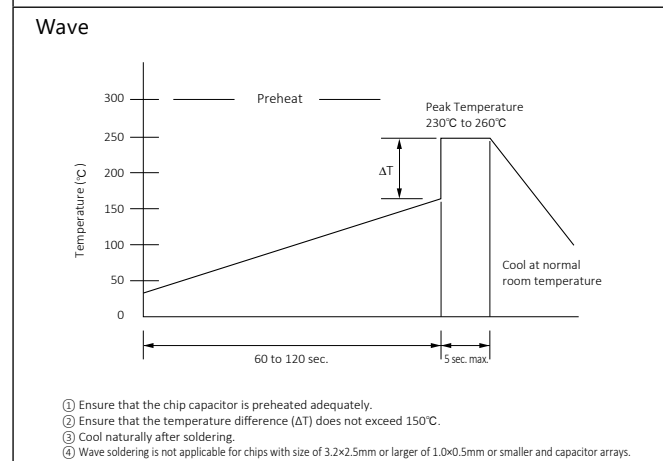
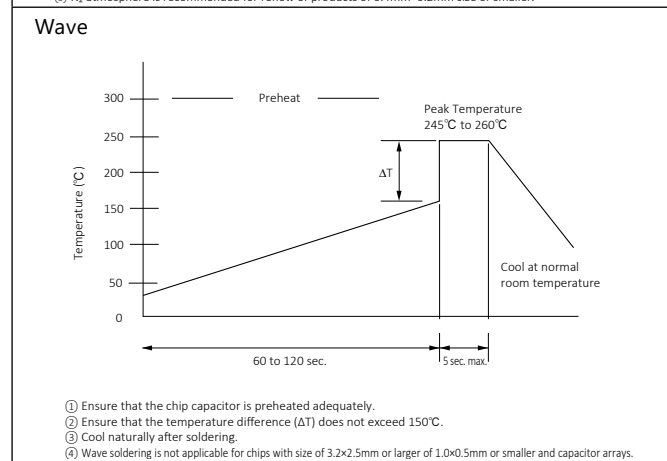
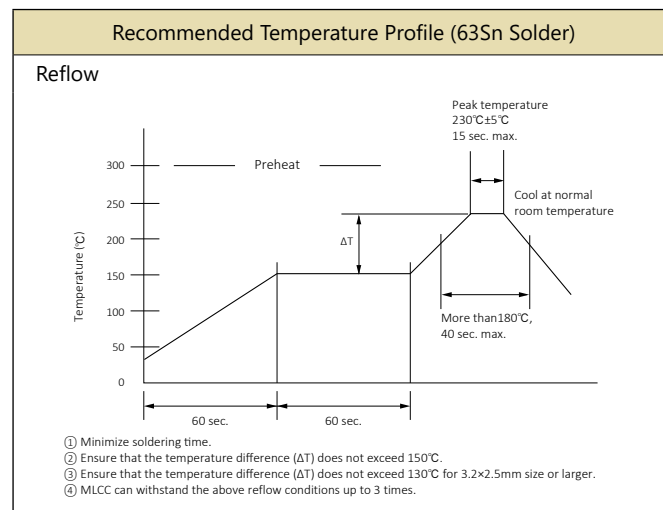
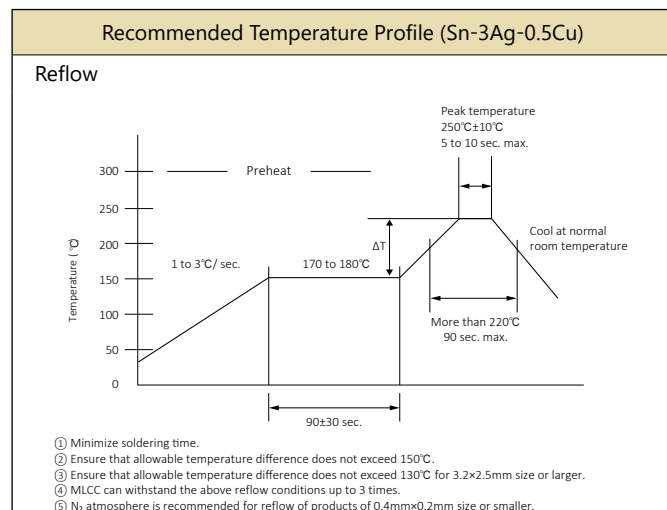
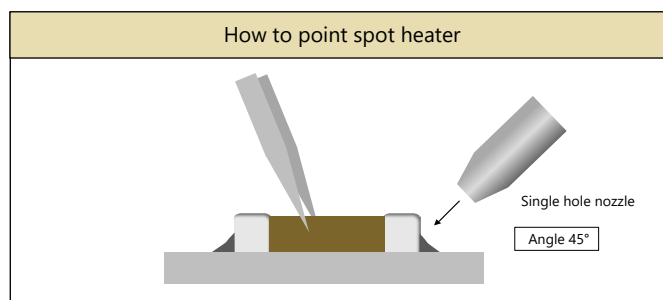
Surface Mounting Information

Soldering Method

- 1) Ceramic is easily damaged by rapid heating or cooling. If some heat shock is unavoidable, preheat enough to limit the temperature difference (ΔT) to within 150 degree Celsius.
- 2) The product size 1.6×0.8mm to 3.2×1.6mm can be used in reflow and wave soldering, and the product size of bigger than 3.2×1.6mm, or smaller than 1.6×0.8mm can be used in reflow.
Circuit shortage and smoking can be created by using capacitors which are used neglecting the above caution.
- 3) Please see our recommended soldering conditions.
- 4) In case of using Sn-Zn Solder, please contact us in advance.
- 5) The following condition is recommended for spot heater application.

• Recommended spot heater condition

Item	Condition
Distance	5mm min.
Angle	45°
Projection Temp.	400°C max.
Flow rate	Set at the minimum
Nozzle diameter	2φ to 4φ (Single hole type)
Application time	10 sec. max. (1206 and smaller) 30 sec. max. (1210 and larger)





Precautions

Circuit Design

1. Once application and assembly environments have been checked, the capacitor may be used in conformance with the rating and performance which are provided in both the catalog and the specifications. Use exceeding that which is specified may result in inferior performance or cause a short, open, smoking, or flaming to occur, etc.
2. Please consult the manufacturer in advance when the capacitor is used in devices such as: devices which deal with human life, i.e. medical devices; devices which are highly public orientated; and devices which demand a high standard of liability.
Accident or malfunction of devices such as medical devices, space equipment and devices having to do with atomic power could generate grave consequence with respect to human lives or, possibly, a portion of the public. Capacitors used in these devices may require high reliability design different from that of general purpose capacitors.
3. Please use the capacitors in conformance with the operating temperature provided in both the catalog and the specifications.
Be especially cautious not to exceed the maximum temperature. In the situation the maximum temperature set forth in both the catalog and specifications is exceeded, the capacitor's insulation resistance may deteriorate, power may suddenly surge and short-circuit may occur.
The capacitor has a loss, and may self-heat due to equivalent series resistance when alternating electric current is passed therethrough. As this effect becomes especially pronounced in high frequency circuits, please exercise caution.
When using the capacitor in a (self-heating) circuit, please make sure the surface of the capacitor remains under the maximum temperature for usage. Also, please make certain temperature rises remain below 20°C.
4. Please keep voltage under the rated voltage which is applied to the capacitor. Also, please make certain the peak voltage remains below the rated voltage when AC voltage is super-imposed to the DC voltage.
In the situation where AC or pulse voltage is employed, ensure average peak voltage does not exceed the rated voltage.
Exceeding the rated voltage provided in both catalog and specifications may lead to defective withstanding voltage or, in worst case situations, may cause the capacitor to smoke or flame.
5. When the capacitor is to be employed in a circuit in which there is continuous application of a high frequency voltage or a steep pulse voltage, even though it is within the rated voltage, please inquire to the manufacturer. In the situation the capacitor is to be employed using a high frequency AC voltage or a extremely fast rising pulse voltage, even though it is within the rated voltage, it is possible capacitor reliability will deteriorate.
6. It is a common phenomenon of high-dielectric products to have a deteriorated amount of static electricity due to the application of DC voltage.
Due caution is necessary as the degree of deterioration varies depending on the quality of capacitor materials, capacity, as well as the load voltage at the time of operation.
7. Do not use the capacitor in an environment where it might easily exceed the respective provisions concerning shock and vibration specified in the catalog and specifications.
In addition, it is a common piezo phenomenon of high dielectric products to have some voltage due to vibration or to have noise due to voltage change. Please contact sales in such case.
8. If the electrostatic capacity value of the delivered capacitor is within the specified tolerance, please consider this when designing the respective product in order that the assembled product function appropriately.
9. Please contact us upon using conductive adhesives.

Storage

Please note the following regarding the storage of delivered products.

1. Set the storage temperature to + 5 to + 40 °C and humidity to 20 ~ 70% RH. Other meteorological conditions are in accordance with classification 1 K2 of JIS C 60721 -3 -1.
2. Store in a place where corrosive gas (H₂S, SO₂, NO₂, Cl₂, etc.) does not exist in the atmosphere. Also, avoid exposure to salty moisture. In either case, this may cause oxidation corrosion of the terminal electrode, reducing solderability.

If you store the above delivered products according to the conditions listed above, it will satisfy the solderability standard for 6 months from the shipping date.


Safety application guideline and detailed information of electrical properties are also provided in kyocera web site;
URL: <https://ele.kyocera.com/en/product/capacitor/>



Part Number List

 Automotive AR05 Series Size (JIS Code) : 0402(1005) # Packaging Code (Packaging quantity) : H(10,000pcs.) / N(50,000pcs.)

Dielectric code	Capacitance	□:Tolerance	Voltage [V]	Part Number	Tanδ [%]	Dimension			# Packaging Code (quantity)
						L[mm]	W[mm]	T[mm]	
X7R	1μF	K:±10% / M:±20%	6.3	AR05X7R105□06A#	7.5	1.0±0.05	0.5±0.05	0.5±0.05	H/N

 Automotive AR21 Series Size (JIS Code) : 0805(2012) # Packaging Code (Packaging quantity) : T(3,000pcs.) / L(10,000pcs.)

Dielectric code	Capacitance	□:Tolerance	Voltage [V]	Part Number	Tanδ [%]	Dimension			# Packaging Code (quantity)
						L[mm]	W[mm]	T[mm]	
X7S	10μF	K:±10% / M:±20%	10	AR21X7S106□10A#	10.0	2.0±0.2	1.25±0.2	1.25±0.2	T/L



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