Multilayer Ceramic Chip Capacitors How to Order



■Features

- •Kyocera's series of Multilayer Ceramic Chip Capacitors are designed to meet a wide variety of needs. We offer a complete range of products for both general and specialized applications.
- •We have a network worldwide in order to supply our global customer bases quickly and efficiently.
- •All our products are highly reliable due to their monolithic structure of high-purity and superfine uniform ceramics and their integral internal electrodes.
- Our stringent quality control in every phase of production from material procurement to shipping ensures consistent manufacturing and superior quality.
- Kyocera components are available in a wide choice of dimensions, temperature characteristics, rated voltages, and terminations to meet specific configurational requirements.



e.g.)

KGM 0J M

(5) (7)**(6)**

(8) Option Code (When needed)

: KGM Series(General) Series
 Size (EIA)

0201 ③ Thickness (max.): 0.39mm

: Operating Temperature Range: -55 to 85°C/ ΔC max.: ±15%/ Standard Temperature: 25°C

: 6.3Vdc : 2.2µF (5) Rated Voltage 6 Capacitance7 Tolerance : ±20%

: Taping Material Paper/ Taping Width 8mm/ (8) Packaging

Cavity Pitch 2mm/ Reel Size φ180

1 Series Code

CODE	Type	
KGM	General	
KGT	Low Profile	
KGU	High-Q	
KAM	Automotive	
KGN	Three Terminal Capacitors	

② Size Code

CODE	EIA	JIS
02	01005	0402
03	0201	0603
05	0402	1005
15	0603	1608
21	0805	2012
31	1206	3216
32	1210	3225

3 Thickness (max.)

CODE	EIA	JIS	Thickness Code	Thickness(max.)
02	01005	0402	Α	0.22
			Α	0.33
			В	0.35
03	0201	0603	С	0.39
			D	0.55
			Υ	0.22
			Α	0.55
			В	0.65
05	0402	1005	С	0.7
			D	0.8
			X	0.22
			Υ	0.33
			Z	0.5
15	0603	1608	Α	0.9
13	0003	1000	С	1.0
21	0805	2012	Α	1.45
21	0003	2012	С	0.95
			Α	1.8
31	1206	206 3216	F	1.75
31	1206		Н	1.9
			L	0.95
32	1210	3225	А	2.7

(5) Voltage Code

CODE	Rated Voltage	CODE	Rated Voltage
0E	2.5Vdc	1E	25Vdc
0G	4Vdc	1V	35Vdc
OJ	6.3Vdc	1H	50Vdc
1A	1A 10Vdc		100Vdc
1C	16Vdc		

6 Capacitance Code

Capacitance expressed in pF. Two significant digits plus number of zeros. For Values < 10pF, Letter R denotes decimal point,

(Example)

<u> </u>	<u> </u>		
CODE	Capacitance	CODE	Capacitance
R50	0.5pF	103	10000pF
1R0	1pF	104	0.1µF
100	10pF	105	1µF
101	100pF	106	10µF
102	1000pF	107	100µF

(7) Tolerance Code

	Temperature Compensation Type(CG/CH)						
CODE	Tolerance						
A ±0.05pF							
B ±0.1pF							
C ±0.25pF							
D	D ±0.5pF						
G	G ±2%						
J	J ±5%						
K	±10%						

(R5,	High Dielectric Constant Type (R5/S6/T6/R7/K7/S7/T7)				
CODE	CODE Tolerance				
J*	J* ±5%				
K	K ±10%				
М	±20%				

^{* :} Option

4 Dielectric Code

	Temperature Compensation Type								
CODE	CODE Temperature Range(°C) ppm/°C								
CG	FF - 12F	CG −55 ~ 125 0		±30					
CH	-33.~ 123	U	±60						

·All parts of COG will be marked as "CG" but will conform to the above table.

Temperature coefficients are determined by calculation based on measurement at 20°C and 85°C.

	High Dielectric Constant Type								
CODE	Temperature Range(°C)	ΔC (%)	Reference Temp.°C						
R5	−55 ~ 85	±15							
S6	-55 ∼ 105	±22							
T6	-33 ~ 103	+22/-33							
R7		±15	25						
K7*	-55 ∼ 125	±15							
S7	-33 ~ 123	±22							
T7		+22/-33							

*Special spec: Change in capacitance under 50% of rated voltage applied.

Measurement conditions for temperature characteristics K7.

Applied voltage and Temperature step

Step	С	Applying Voltage	Temperature℃		
1	C0	No bias Reference Temp.			
2	_		Reference Temp.		
3	C1	50% of	Min. Operating Temp.		
4	C2	Rated voltage	Reference Temp.		
5	C1		Max. Operating Temp.		

 $\Delta C/C(\%) = (C1-C2)/C0 \times 100$

C0:Capacitance value at step 1

C1:Capacitance value from step 3 to 5

C2:Capacitance value at step 4

Packaging Code

® Pac	8 Packaging Code								
CODE	Size Code	Material	Width	Pitch	Reel size				
Т	15 to 31	Paper	8mm	4mm					
Н	02 to 05	Paper	8mm	2mm					
Q	03	Paper	8mm	1mm	φ180				
U	21 to 32	Plastic	8mm	4mm					
Р	02	Plastic	4mm	1mm					
М	15 / 21	Paper	8mm	4mm					
N	02 to 05	Paper	8mm	2mm	φ330				
W	03	Paper	8mm	1mm	ψοσο				
L	21 to 32	Plastic	8mm	4mm					

Multilayer Ceramic Chip Capacitors Dimension

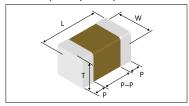


1mm

2mm

4mm

■KGM/KGT/KGU/KAM Series (Two Terminal Capacitors)



■ Packaging Code 20kp (E 8 / 2) kp means Taping Material Taping Width Pitch 1000 pieces Code Material Code Width Code Pitch

4mm

8mm

8

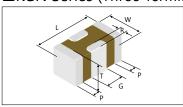
Plastic

Paper

Size	Со	de	Thickness		Dimension (mm)					Quantity	per reel																																	
Size	EIA	JIS	Code	L	W	Т	P min.	P max.	$P \sim P \text{ min.}$	φ180 Reel	φ330 Reel																																	
02	01005	0402	Α	0.4±0.02	0.2±0.02	0.2±0.02	0.07	0.14	0.13	40kp(E4/1)	_																																	
02	01003	0402	A	0.4±0.02	0.2±0.02	0.2±0.02	0.07	0.14	0.13	20kp(P8/2)	80kp(P8/2)																																	
			Υ	0.6±0.03	0.3±0.03	0.22 max.	0.1	0.2	0.2	30kp(P8/1)	1E0km/D0 /1)																																	
			Α	0.6±0.03	0.5±0.05	0.3±0.03	0.1	0.2	0.2		150kp(P8/1)																																	
			В	0.6±0.05	0.3±0.05	0.3±0.05				15kp(P8/2)	50kp(P8/2)																																	
03	0201	0603	Υ*			0.22 max.	0.13	0.23	0.19	15kp(P8/2)	50kp(P8/2)																																	
			С	0.6±0.09	0.3±0.09	0.3±0.09	0.13	0.23	0.19	13KP(F6/2)	30KP(F6/2)																																	
			D	0.0±0.09	0.5±0.09	0.5±0.05				10kp(P8/2)																																		
			D*			0.3±0.03	0.1	0.25	0.19	10κμ(+6/2)	_																																	
		Y		1.0±0.05	0.5±0.05	0.33 max.				10kp(P8/2)	50kp(P8/2)																																	
						0.5±0.05	0.15 0.35																																					
			X	1.0±0.1	0.5±0.05	0.22 max.				10kp(P8/2)	50kp(P8/2)																																	
		0402 1005	В	1.0±0.15	0.5±0.15	0.5±0.15				10kp(P8/2)	40kp(P8/2)																																	
05	0402		Υ*			0.33 max.		0.35	5 0.3	10kp(P8/2)	_																																	
			Z			0.5 max.]			10kp(P8/2)	50kp(P8/2)																																	
			Α*	1.0±0.2 0.5±0.2	1.0±0.2	1.0 ± 0.2	1.0±0.2	1.0±0.2	1.0±0.2	1.0±0.2	0.5±0.2	0.55 max.]			10kp(P8/2)	50kp(P8/2)																											
			С			0.5±0.2																																						
			D			0.8 max.				10kp(P8/2)	30kp(P8/2)																																	
15	0603	1608	Α	1.6±0.1	0.8±0.1	0.8±0.1	0.2	0.6	0.6	0.6	0.6	0.6	0.6	0.5	4kp(P8/4)	10kp(P8/4)																												
13	0003	1000	С	1.6±0.2	0.8±0.2	0.8±0.2	0.2	0.0	0.5																																			
21	0805	305 2012	С	2.0±0.2	1.25±0.2	0.95 max.	0.2	0.2 0.75	0.2 0.75 0	0.7	4kp(P8/4)	10kp(P8/4)																																
21	0003	2012	Α			1.25±0.2	0.2	0.73	0.7	3kp(E8/4)	10kp(E8/4)																																	
			L	3.2±0.2	1.6±0.2	0.95 max.]			4kp(P8/4)	_																																	
31	1206	3216	F	3.2±0.2	1.6±0.15	1.6±0.15	0.3	0.85	1.4	2.5kp(E8/4)	5kp(E8/4)																																	
31	1200	3210	Α		1.6±0.2	1.6±0.2				2.3KP(L0/4)	3KP(L0/4)																																	
			Н	3.2±0.3	1.6±0.3	1.6±0.3	0.3	0.85	1.9	2kp(E8/4)	_																																	
32	1210	3225	Α	3.2±0.3	2.5±0.2	2.5±0.2	0.3	1.0	1.4	1kp(E8/4)	4kp(E8/4)																																	

* If there is a "*" in the thickness code indicates the same thickness (T-dimension) but different L/W or P-dimension. Please refer to the part number list for details.

■KGN Series (Three Terminal Capacitors)



	Size	Code Thickness					Dimension (Quantity per reel				
	Size	EIA	JIS	Code	L	W	T	G	Р	R	φ180 Reel	φ330 Reel
Γ	KGN	0402 1005	Z	1.0±0.1	0.5±0.2	0.5 max.						
	05		1005	В	1.0±0.15	0.5±0.15	0.5±0.15	0.3±0.1	0.15±0.1	≥0.05	10kp(P8/2)	_
	05			C.	1.0±0.2	0.5±0.2	0.5±0.2	- 0.0_0			•	

Multilayer Ceramic Chip Capacitors



Automotive KAM Series



■ Features

With our unique materials and manufacturing technology, we provide products that fully bring out the performance of equipment even in the highly reliable environment required for automotive application.

■Applications

•ECU,ADAS,ESC,ABS,LCD panel

R7/S7/T7 Dielectric



●Capacitance chart ■ Standard Spec.1												
				R7								
Capacita	ance	104	224	474	105	225	475	106				
Size/Voltag	je(Vdc)	0.1µF	0.22µF	0.47µF	1μF	2.2µF	4.7μF	10μF				
1/ 1 1 102	2.5	Α7										
(0201)	4	Α7										
(0201)	6.3	Α7										
	2.5			A7	A5							
KAM05	4			A7	A5							
(0402)	6.3			A7	A5							
	10			Α7	C7							
	2.5				A7	A7	C7					
LANA1 F	4				_A7	A7	C7					
(0603)	6.3				Α7	A7	C7					
(5505)	10			l	A7	A7						
	16				Α7							
KAM21	10							Δ7				

				S7				
Capacita	ance	104	224	474	105	225	475	106
Size/Voltag	e(Vdc)	0.1μF	0.22µF	0.47µF	1μF	2.2µF	4.7μF	10μF
KAM21 (0805)	10							Α7

				T7	7				
Capacita	ance	104	224	474	105	225	475	106	226
Size/Voltag	je(Vdc)	0.1μF	0.22µF	0.47µF	1μF	2.2µF	4.7μF	10μF	22µF
	2.5		Α8	C8	C8				
KAM03	4		A8	C8	C8				
(0201)	6.3		A8	C8	C8				
	10			C8					
	2.5					C8	C8	C8	
KAM05	4					C8	C8	C8	
(0402)	6.3					C8	C8	,	
	10					C8			
KAM15	2.5							C8	C8
(0603)	4							C8	C8
(0000)	6.3							C8	C8
VA 1 1 2 1	2.5						l		A8
(0805)	4								A8
(1303)	6.3								Α8

Please contact for capacitance value other than standard.

Please refer to here for the test method and specifications of Standard Specification 1.

The code in the capacity range table means product thickness (T-dimension) and Tan delta. For details about T dimensions, please refer to the Dimension section in the parts number list below. For Tan delta, please refer to the list on the right.

(Example) In case of "A5" for KAM05; T: 0.5 ± 0.05 mm, Tan δ : 7.5% max.

Tan δ Code	Tan δ
5	7.5% max.
7	10.0% max.
8	12.5% max.

Multilayer Ceramic Chip Capacitors



Automotive KAM Series



Parts number list Automotive KAM03~21 Series Temperature Characteristic: R7 Tolerance □: K: ± 10%/ M: ±20%

Thisluses			Tolerance	Voltage	D:	mension[mr	m]				Packag	ging: #			
Thickness code	Part Number	Capacitance	:□	-	וט	mensionimi	11]		Ф1	80			Ф3	30	
Couc				[V]	L	W	T	code	QTY	code	QTY	code	QTY	code	QTY
A7	KAM03AR70J104□#	0.1µF	K/M	6.3	0.6±0.03	0.3±0.03	0.3±0.03	Н	15kp	Q	30kp	N	50kp	W	150kp
A7	KAM03AR70G104□#	0.1µF	K/M	4	0.6±0.03	0.3±0.03	0.3±0.03	Н	15kp	Q	30kp	N	50kp	W	150kp
A7	KAM03AR70E104□#	0.1µF	K/M	2.5	0.6±0.03	0.3±0.03	0.3 ± 0.03	Н	15kp	Q	30kp	N	50kp	W	150kp
A7	KAM05AR71A474□#	0.47µF	K/M	10	1.0±0.05	0.5±0.05	0.5±0.05	Н	10kp	-	ı	Ν	50kp	_	_
C7	KAM05CR71A105□#	1μF	K/M	10	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	-	-	N	40kp	_	_
A7	KAM05AR70J474□#	0.47µF	K/M	6.3	1.0±0.05	0.5±0.05	0.5±0.05	Н	10kp	_	-	N	50kp	_	_
A5	KAM05AR70J105□#	1μF	K/M	6.3	1.0±0.05	0.5±0.05	0.5±0.05	Н	10kp	-	-	N	50kp	_	_
A7	KAM05AR70G474□#	0.47µF	K/M	4	1.0±0.05	0.5±0.05	0.5±0.05	Н	10kp	-	ı	N	50kp	_	_
A5	KAM05AR70G105□#	1μF	K/M	4	1.0±0.05	0.5±0.05	0.5±0.05	Н	10kp	_	_	N	50kp	_	-
A7	KAM05AR70E474□#	0.47µF	K/M	2.5	1.0±0.05	0.5±0.05	0.5±0.05	Н	10kp	_	_	N	50kp	_	_
A5	KAM05AR70E105□#	1μF	K/M	2.5	1.0±0.05	0.5±0.05	0.5±0.05	Н	10kp	_	-	N	50kp	_	_
A7	KAM15AR71C105□#	1μF	K/M	16	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	-	-	М	10kp	_	_
A7	KAM15AR71A105□#	1μF	K/M	10	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	_	_	М	10kp	_	-
A7	KAM15AR71A225□#	2.2µF	K/M	10	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	_	_	М	10kp	_	-
A7	KAM15AR70J105□#	1µF	K/M	6.3	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	_	_	М	10kp	_	-
A7	KAM15AR70J225□#	2.2µF	K/M	6.3	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	_	_	М	10kp	_	_
C7	KAM15CR70J475□#	4.7µF	K/M	6.3	1.6±0.2	0.8±0.2	0.8±0.2	T	4kp	_	-	М	10kp	_	_
A7	KAM15AR70G105□#	1μF	K/M	4	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	-	-	М	10kp	_	_
A7	KAM15AR70G225□#	2.2µF	K/M	4	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	_	_	М	10kp	_	-
C7	KAM15CR70G475□#	4.7µF	K/M	4	1.6±0.2	0.8±0.2	0.8±0.2	T	4kp	_	_	М	10kp	_	-
A7	KAM15AR70E105□#	1μF	K/M	2.5	1.6±0.1	0.8±0.1	0.8±0.1	T	4kp	_	_	М	10kp	_	_
A7	KAM15AR70E225□#	2.2µF	K/M	2.5	1.6±0.1	0.8±0.1	0.8±0.1	Т	4kp	_	_	М	10kp	_	-
C7	KAM15CR70E475□#	4.7µF	K/M	2.5	1.6±0.2	0.8±0.2	0.8±0.2	Т	4kp	_	-	М	10kp	_	_
A7	KAM21AR71A106□#	10μF	K/M	10	2.0±0.2	1.25±0.2	1.25±0.2	U	3kp	1	ı	Ĺ	10kp	_	_

Parts number list Automotive KAM21 Series Temperature Characteristic: S7 Tolerance □: K: ± 10%/ M: ±20%

	The sales are a			Tolerance	Valtaga	D	mansianImr	m1		Packag						
-	Thickness code	Part Number	Capacitance	iolerance	5 -			Ф180				Ф330				
	code	e		.⊔	[V]	L	W	T	code	QTY	code	QTY	code	QTY	code	QTY
	A7	KAM21AS71A106□#	10μF	K/M	10	2.0±0.2	1.25±0.2	1.25±0.2	U	3kp	_	_	L	10kp	_	_

Parts number list Automotive KAM03~21 Series Temperature Characteristic: T7 Tolerance □: K: ± 10%/ M: ±20%

Thickness			Tolerance	Voltage	Di	imension[mr	ml				Packag	ging: #			
Thickness code	Part Number	Capacitance	:□	[V]	Di	IIII	11]		Ф1	80			Ф3	30	
couc				[v]	L	W	T	code	QTY	code	QTY	code	QTY	code	QTY
C8	KAM03CT71A474□#	0.47µF	K/M	10	0.6±0.09	0.3±0.09	0.3±0.09	Н	15kp	-	_	N	50kp	_	
A8	KAM03AT70J224□#	0.22µF	K/M	6.3	0.6±0.03	0.3±0.03	0.3±0.03	Н	15kp	-	_	N	50kp	_	_
C8	KAM03CT70J474□#	0.47µF	K/M	6.3	0.6±0.09	0.3±0.09	0.3±0.09	Н	15kp	_	_	N	50kp	_	_
C8	KAM03CT70J105□#	1μF	K/M	6.3	0.6±0.09	0.3±0.09	0.3±0.09	Н	15kp	_	_	N	50kp	_	_
A8	KAM03AT70G224□#	0.22µF	K/M	4	0.6±0.03	0.3±0.03	0.3±0.03	Н	15kp	_	ı	N	50kp	_	_
C8	KAM03CT70G474□#	0.47µF	K/M	4	0.6±0.09	0.3±0.09	0.3±0.09	Н	15kp	-	-	N	50kp	_	_
C8	KAM03CT70G105□#	1μF	K/M	4	0.6±0.09	0.3±0.09	0.3±0.09	Н	15kp	-	_	N	50kp	_	_
A8	KAM03AT70E224□#	0.22µF	K/M	2.5	0.6±0.03	0.3±0.03	0.3±0.03	Н	15kp	_	_	N	50kp	_	_
C8	KAM03CT70E474□#	0.47µF	K/M	2.5	0.6±0.09	0.3±0.09	0.3±0.09	Н	15kp	_	-	N	50kp	_	_
C8	KAM03CT70E105□#	1μF	K/M	2.5	0.6±0.09	0.3±0.09	0.3±0.09	Н	15kp	_	-	N	50kp	_	_
C8	KAM05CT71A225□#	2.2µF	K/M	10	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	_	-	N	40kp	_	_
C8	KAM05CT70J225□#	2.2µF	K/M	6.3	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	_	_	N	40kp	_	_
C8	KAM05CT70J475□#	4.7µF	K/M	6.3	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	_	_	N	40kp	_	_
C8	KAM05CT70G225□#	2.2µF	K/M	4	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	-	-	N	40kp	_	_
C8	KAM05CT70G475□#	4.7µF	K/M	4	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	_	-	N	40kp	_	_
C8	KAM05CT70G106□#	10µF	K/M	4	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	_	ı	N	40kp	_	_
C8	KAM05CT70E225□#	2.2µF	K/M	2.5	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	_	_	N	40kp	_	
C8	KAM05CT70E475□#	4.7µF	K/M	2.5	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	_	_	N	40kp	_	_
C8	KAM05CT70E106□#	10µF	K/M	2.5	1.0±0.2	0.5±0.2	0.5±0.2	Н	10kp	-	_	N	40kp	_	_
C8	KAM15CT70J106□#	10µF	K/M	6.3	1.6±0.2	0.8±0.2	0.8±0.2	Т	4kp	-	-	М	10kp	_	_
C8	KAM15CT70J226□#	22µF	K/M	6.3	1.6±0.2	0.8±0.2	0.8±0.2	Т	4kp	_	-	М	10kp	_	_
C8	KAM15CT70G106□#	10µF	K/M	4	1.6±0.2	0.8±0.2	0.8±0.2	T	4kp	1	ı	М	10kp	_	_
C8	KAM15CT70G226□#	22µF	K/M	4	1.6±0.2	0.8±0.2	0.8±0.2	Т	4kp	_	_	М	10kp	_	_
C8	KAM15CT70E106□#	10µF	K/M	2.5	1.6±0.2	0.8±0.2	0.8±0.2	T	4kp	_	_	М	10kp	_	_
C8	KAM15CT70E226□#	22µF	K/M	2.5	1.6±0.2	0.8±0.2	0.8±0.2	T	4kp	-	-	М	10kp	_	_
A8	KAM21AT70J226□#	22µF	K/M	6.3	2.0±0.2	1.25±0.2	1.25±0.2	U	3kp	_	_	L	10kp	_	_
A8	KAM21AT70G226□#	22µF	K/M	4	2.0±0.2	1.25±0.2	1.25±0.2	U	3kp	_	_	L	10kp	_	-
A8	KAM21AT70E226□#	22µF	K/M	2.5	2.0±0.2	1.25±0.2	1.25±0.2	U	3kp	-	-	L	10kp	_	_

Multilayer Ceramic Chip Capacitors Test Conditions and Standards



Specifications and Test Conditions for High Dielectric Type (R7, S7, T7) KAM Series (Standard Spec.1)

Test	Items	Specifications	Test Conditions (Complies with AEC-Q200)								
Temperature Cycle	Appearance	No problem observed	Take the initial value after heat treatment. 1 cycle: refer to the table on the right. Step Temperature (°C) Time (min.)								
-, -:-	Capacitance Variation	Within±10.0%	Number of cycles : 1,000cycles JESD22 Method JA-104 1 -55+0/-3 30±3								
	Ταηδ	Within tolerance	Measurement after heat treatment. 2 Room temperature 1 3 125+3/-0 30±3								
	Insulation Resistance	Over 50MΩ · μF	4 Room temperature 1 The charge and discharge current of the capacitor must not exceed 50mA.								
Load Humidity	Appearance	No problem observed	Take the initial value after heat treatment. Temperature: 85±3°C								
	Capacitance Variation	Within±12.5%	Humidity: 80~85%RH Voltage: Rated voltage								
	Tanδ	200% max. of initial value	Time: 1000±12h MIL-STD-202 Method 103								
	Insulation Resistance	Over 10MΩ • μF	Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.								
Load Life	Appearance	No problem observed	Take the initial value after heat treatment.								
	Capacitance Variation	Within±12.5%	Temperature : 125±3°C Voltage : Rated voltage Time : 1000±12h								
	Tanδ	200% max. of initial value	MIL-STD-202 Method 108								
	Insulation Resistance	Over 10MΩ · μF	Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.								
Appearance		No problem observed	External Visual MIL-STD-883 Method 2009								
Dimensions		Refer to capacitance chart	Physical Dimensions JESD22 Method JB-100								
	Appearance	No problem observed	Take the initial value after heat treatment. Pulse: half sine shock pulse Time: 6ms								
Mechanical	Capacitance	Within tolerance	Maximum shock: 100Ġ Speed: 3.75m/s Shock times: Three shocks in each direction shall be applied along the								
Shock	Tanδ	Within tolerance	three mutually perpendicular axes of the test specimen (18 shocks). MIL-STD-202 Method 213								
	Insulation Resistance	Over 50MΩ • μF	Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.								
	Appearance	No problem observed	Take the initial value after heat treatment. Vibration frequency: 10Hz~2000Hz~10Hz(20 min.)								
	Capacitance	Within tolerance	Acceleration: 5.0g's Sweep time and duration: This cycle shall be performed 12 times in								
Vibration	Tanδ	Within tolerance	each of three mutually perpendicular directions(total of 36 times). MIL-STD-202 Method 204								
	Insulation Resistance	Over 50MΩ · μF	Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50mA.								
Appearance No pro		No problem observed	Take the initial value after heat treatment.								
Resistance to	Capacitance Variation	Within±10.0%	Reflow: 3 times Peak temperature: 250±5°C Time: 30±5sec								
Solder Heat	Tanδ	Within tolerance	MIL-STD-202 Method 210								
	Insulation Resistance	Over $50 \text{M}\Omega \cdot \mu\text{F}$	Measurement after heat treatment. The charge and discharge current of the capacitor must not exceed 50m/								

Multilayer Ceramic Chip Capacitors Test Conditions and Standards



Specifications and Test Conditions for High Dielectric Type (R7, S7, T7) KAM Series (Standard Spec.1)

Test	tems	Specifications	Tes	t Conditions	(Compli	es with	h AEC-Q200))		
	Appearance	No problem observed								
	Capacitance	Within tolerance	 Take the initial value after heat treatment. AEC-Q200-002 Voltage level : 2kV Measurement after heat treatment. 							
ESD	Tanδ	Within tolerance								
	Insulation Resistance	Over 50MΩ • μF	The charge and discharge current of the capacitor must not exceed							
Solderability	Appearance	Solder coverage : 95% min.	Pretreatment: 155+0/-10°C left for 4h±10minutes Flux: Rosin-Ethanol(25wt%) Solder composition: Sn-3.0Ag-0.5Cu(SAC305) Solder melting temperature: 245±5°C Dipping time: 5±0.5sec							
	Capacitance	Within tolerance	Measure afte	r heat treatme	nt.					
			Capacitance	Rated Voltage	Measuri Frequer	_	Measuring Voltage			
	Tanδ	Refer to capacitance chart	C≤10µF	≥ 10Vdc ≤ 6.3Vdc	1kHz±1 1kHz±1	0%	1.0±0.2Vrms 0.5±0.1Vrms			
Electrical Characterization		,	C>10µF	ALL	120Hz±1		0.5±0.1Vrms			
	Insulation Resistance	Over 50MΩ • μF	one minute a	nd then measi	ured.		voltage is app			
	Dielectric Strength	Resist without problem	Apply 2.5 tim	es the rated vo	oltage for	1~5 sec.				
	Appearance	No problem observed	Take the initia	al value after h	eat treatm	ent.				
Bending	Capacitance Variation	Within±10.0%	Pressing spec Flexible volur							
Strength	Tanδ	Within tolerance	Pressing time AEC-Q200-00	e : 60+5/-0 sec 05						
	Insulation Resistance	Over 50MΩ · μF	The charge ar	nd discharge cu	rrent of the	capacito	or must not exc	eed 50mA.		
	Appearance	No problem observed	Take the initial	al value after h	eat treatm	ent.				
Termination	Capacitance	Within tolerance	KAM03,KAM0	05 : 2N						
Strength	Tanδ	Within tolerance	Pressing time	e : 60±1sec						
	Insulation Resistance	Over 50MΩ • μF	AEC-Q200-00 The charge ar		rrent of the	capacito	or must not exc	eed 50mA.		
			Reference ter	range : -55~+ mperature : 25	°C					
Temperature	Capacitance	R7: Within±15%	Capacitance	Measuring fr			uring voltage			
characteristics	Variation	S7 : Within±22% T7 : Within+22/-33%	C≤10µF C>10µF	1kHz±1 120Hz±1			±0.02Vrms ±0.02Vrms			
							ited measurem	ent		
			condition.	ieu neiuw sildii	apply eac	ii iiiuica	iteu illeasuieill	CIIL		

Heat treatment	Expose sample in the temperature of 150+0/ –10°C for 1 hour and leave the sample in normal temperature and
Heat treatment	humidity for 24±2 hours.

Measuring condition for Temperature characteristics.

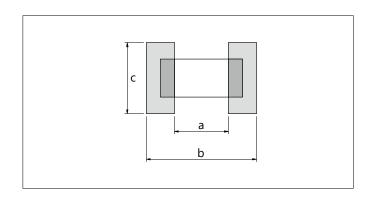
Products	Measuring frequency	Measuring voltage					
KAM03AR70J224,KAM03AR70G224,KAM03AR70E224	1kHz±10%						
KAM03CT71A474,KAM03CT70J474,KAM03CT70G474,KAM03CT70E474	IKHZ±10%	0.2±0.02Vrms					
KAM03CT70J105,KAM03CT70G105,KAM03CT70E105	1kHz±10%	0.08±0.02Vrms					
KAM21AR71A106	1kHz±10%	0.2±0.1Vrms					
KAM21AS71A106	1kHz±10%	1.0±0.2Vrms					

Multilayer Ceramic Chip Capacitors Test Conditions and Standards



Substrate for Adhesion Strength Test, Vibration Test, Soldering Heat Resistance Test, Temperature Cycle Test, Load Humidity Test, High-Temperature with Loading Test.

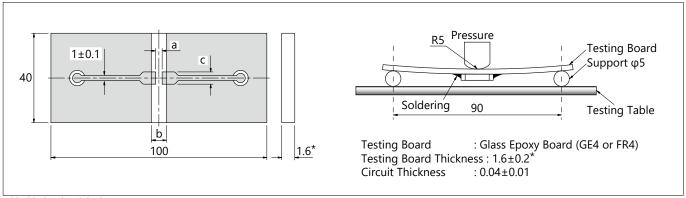
(Unit: mm)



Size (EIA Code)	a	b	С
02 (01005)	0.15	0.5	0.2
03 (0201)	0.26	0.92	0.32
05 (0402)	0.4	1.4	0.5
15 (0603)	1.0	3.0	1.2
21 (0805)	1.2	4.0	1.65
31 (1206)	2.2	5.0	2.0
32 (1210)	2.2	5.0	2.9

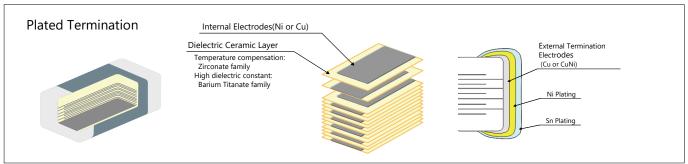
Substrate for Bending Test

(Unit: mm)



* 02, 03, 05 size 0.8±0.1mm

Structure



■Certification status

<ISO>

Acquired ISO 9001 quality management system certification.

<IATF:

Acquired IATF 16949 quality management system certification.

■Production plant

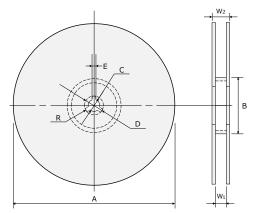
Kagoshima Kokubu plant

Multilayer Ceramic Chip Capacitors Packaging Options Tape and Reel



(Unit: mm)

Reel

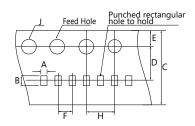


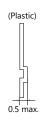
Code Reel	А	В	С	D	
7-inch Reel (CODE: T, H, Q, U)	180 +0				
7-inch Reel (CODE: P)	178±2.0	φ60 min.	13±0.5	21±0.8	
13-inch Reel (CODE: L, M, N, W)	330±2.0				
Code Reel	E	W1	W2	R	
7-inch Reel (CODE: T, H, Q, U)		10.5±1.5	16.5 max.		
7-inch Reel (CODE: P)	2.0±0.5	4.35±0.3	6.95±1.0	1.0	
13-inch Reel (CODE: L. M. N. W)		9.5±1.0	16.5 max.		

Carrier Tape (Unit: mm)

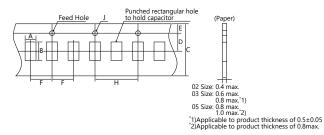
F=1mm (02 Size)

F=1mm (03 Size)

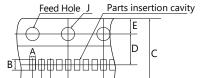


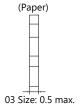


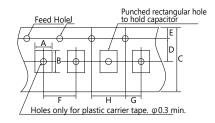
F=2mm (02, 03, 05 Size)

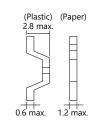


F=4mm (15, 21, 31, 32 Size)









(Unit: mm)

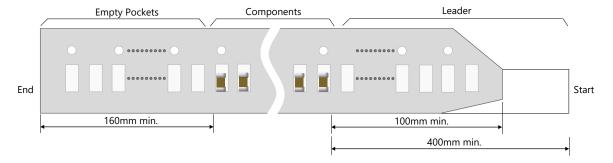
Size (EIA Code)	А	В	С	D	E	F	G	Н	J	Carrie Width	r Tape Material
02 (01005)*	0.24±0.02	0.44±0.02	4.0±0.08	1.8±0.02	0.9±0.05	1.0±0.02		2.0±0.04	0.8±0.04	4	Plastic
02 (01005)*	0.25±0.03	0.45±0.03	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05	-	4.0±0.1	1.5+0.1/-0	8	Paper
	0.3710.03	7 ± 0.03 0.67 ±0.03 8.0 ± 0.3 8.0 ± 0.3 3.5 ±0.05 1.75 ±0.1 1.0 ± 0.05 2.0 ± 0.05 —	8.0+0.3/-0.1	2 5 4 0 05	1 75 + 0 1	1.0±0.05		4.0±0.05	1.5+0.1/-0		
	0.37±0.03		<u> </u>	4.0±0.1	1.5+0.1/-0						
03 (0201)*	0.39±0.03	0.69±0.03	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0	8	Paper
	0.42±0.03	0.72±0.03	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0		
	0.44±0.05	0.74±0.05	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05	_	4.0±0.1			
	0.65±0.1	1.15±0.1	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05		40+01	15,01/0		
05 (0402)*	0.75±0.1		0.0±0.5	3.5±0.05	1.75±0.1	2.0±0.03	_	4.0±0.1	1.5+0.1/-0	8	Paper
	0.8±0.1	1.3±0.1	8.0±0.3	3.5±0.05	1.75±0.1	2.0±0.05	_	4.0±0.1	1.5+0.1/-0	1	
15 (0602)*	1.0±0.2	1.8±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0	0	Paper
15 (0603)*	1.1±0.2	1.9±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0) °	гареі
21 (000E)	15+02 22+02	15+02 22+02 00+02 25+005	1.75±0.1 4.0:	4.0±0.1 2.0±0.05	2.0±0.05 4.0±0.1 1.5	1.5+0.1/-0	8	Paper			
21 (0805) 1.5±0.2	0.2 2.3±0.2 8.0±0.3	3.5±0.05 1.75±0.	1.75±0.1		∠.U±U.U3	±0.05 4.0±0.1	1.5+0.1/-0	8	Plastic		
21 (1206)	31 (1206) 2.0±0.2	20+02 26+02 80+0	8.0±0.3	3.5±0.05	1.75±0.1	1 4.0±0.1	0±0.1 2.0±0.05	05 4.0±0.1	1.5+0.1/-0	8	Paper
31 (1206)		206) 2.0±0.2 3.6±0.2 8.0±0.3 3.5±0.05 1.75±0	1.75±0.1	4.U±U.1	2.0±0.03	4.0±0.1	1.3 10.1/-0	8	Plastic		
32 (1210)	2.9±0.2	3.6±0.2	8.0±0.3	3.5±0.05	1.75±0.1	4.0±0.1	2.0±0.05	4.0±0.1	1.5+0.1/-0	8	Plastic

^{*} Option

Multilayer Ceramic Chip Capacitors Packaging Options

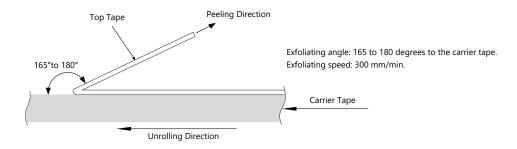


Detail of leader and trailer



Adhesive tape

- 1) The exfoliative strength when peeling off the top tape from the carrier tape by the method of the following figure shall be *0.1 to 0.7N. *02 Size: 0.1 to 0.5N
- 2) When the top tape is peeled off, the adhesive stays on the top tape.
- 3) Chip capacitors will be in a state free without being stuck on the thermal adhesive tape.



Carrier tape

- 1) Chip will not fall off from carrier tape or carrier tape will not be damaged by bending than within a radius of 25mm.
- 2) The chip are inserted continuously without any empty pocket.
- 3) Chip will not be mis-mounted because of too big clearance between components and cavity. Also the waste of carrier tape will not fill a nozzle hole of mounting machine.

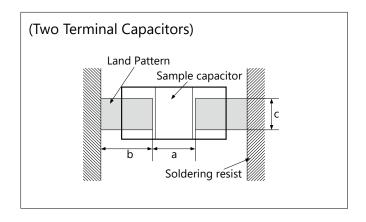
Multilayer Ceramic Chip Capacitors Surface Mounting Information



Dimensions for recommended typical land

Since the amount of solder (size of fillet) to be used has direct influence on the capacitor after mounting, the sufficient consideration is necessary.

When the amounts of solder is too much, the stress that a capacitor receives becomes larger. It may become the cause of a crack in the capacitor. When the land design of printed wiring board is considered, it is necessary to set up the form and size of land pattern so that the amount of solder is suitable.



Two Terminal Capacitors

(Unit: mm)

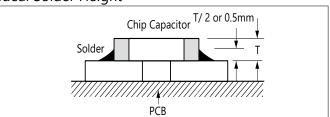
Size	Dime	nsion	Recommended land dimensions			
(EIA Code)	L	W	a	b	С	
02 (01005)	0.4±0.02	0.2±0.02	0.13 to 0.2	0.12 to 0.18	0.2 to 0.23	
	0.6±0.03	0.3±0.03	0.2 to 0.25	0.25 +- 0.25	0.3 to 0.4	
03 (0201)	0.6±0.05	0.3±0.05	0.2 10 0.25	0.25 to 0.35		
	0.6±0.09	0.3±0.09	0.23 to 0.3	0.25 to 0.35	0.3 to 0.45	
	1.0±0.05	0.5±0.05	0.3 to 0.5	0.35 to 0.45	0.4 to 0.6	
05 (0402)	1.0±0.15	0.5±0.15	0.4 to 0.6	0.4 to 0.5	0.5 to 0.75	
	1.0±0.2	0.5±0.2	0.4 10 0.6	0.4 (0 0.5		
15 (0603)	1.6±0.1	0.8±0.1	0.7 to 1.0	0.8 to 1.0	0.6 to 0.9	
15 (0005)	1.6±0.2	0.8 ± 0.2	0.8 to 1.0	0.8 to 1.0	0.8 to 1.1	
21 (0805)	2.0±0.2	1.25±0.2	1.0 to 1.3	1.0 to 1.2	1.25 to 1.55	
	3.2±0.2	1.6±0.15	2.1 to 2.5	1.1 to 1.3	1.4 to 1.9	
31 (1206)	3.2±0.2	1.6±0.2	2.1 to 2.5	1.1 to 1.3	1.6 to 2.0	
	3.2±0.3	1.6±0.3	2.1 (0 2.5	1.1 (0 1.3	1.0 (0 2.0	
32 (1210)	3.2±0.3	2.5±0.2	2.1 to 2.5	1.1 to 1.3	1.9 to 2.8	

^{*} Recommended land dimensions may differ depending on dimensional tolerance.

Design of printed circuit and Soldering

The recommended fillet height shall be 1/2 of the thickness of capacitors or 0.5mm. When mounting two or more capacitors in the common land, it is necessary to separate the land with the solder resist strike so that it may become the exclusive land of each capacitor.

Ideal Solder Height



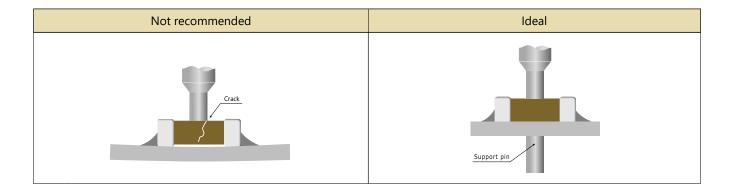
Item	Prohibited	Recommended example : Separation by solder resist
Multiple parts mount		Solder resist
Mount with leaded parts	Leaded parts	Solder resist Leaded parts
Wire soldering after mounting	Soldering iron Wire	Solder resist
Side by side layout	Solder resist	Solder resist

Multilayer Ceramic Chip Capacitors Surface Mounting Information



Actual Mounting

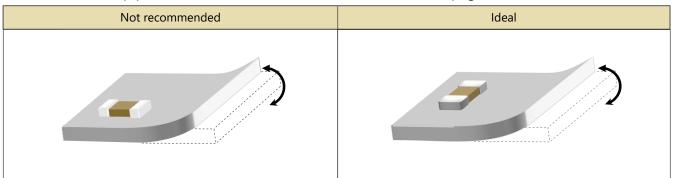
- 1) If the position of the vacuum nozzle is too low, a large force may be applied to the chip capacitor during mounting, resulting in cracking.
- 2) During mounting, set the nozzle pressure to a static load of 1 to 3 N.
- 3) To minimize the shock of the vacuum nozzle, provide a support pin on the back of the PCB to minimize PCB flexure.
- 4) Bottom position of pick up nozzle should be adjusted to the top surface of a substrate which camber is corrected.



Mounting Design

The chip could crack if the PCB warps during processing after the chip has been soldered.

Recommended chip position on PCB to minimize stress from PCB warpage

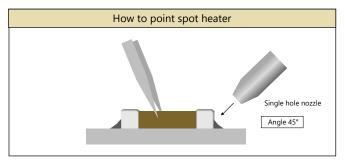


Soldering Method

- 1) Ceramic is easily damaged by rapid heating or cooling. If some heat shock is unavoidable, preheat enough to limit the temperature difference (Delta T) to within 150 degree Celsius.
- 2) The product size 1.6×0.8mm to 3.2×1.6mm can be used in reflow and wave soldering, and the product size of bigger than 3.2×1.6mm, or smaller than 1.6×0.8mm can be used in reflow.
 - Circuit shortage and smoking can be created by using capacitors which are used neglecting the above caution.
- 3) Please see our recommended soldering conditions.
- 4) In case of using Sn-Zn Solder, please contact us in advance.
- 5) The following condition is recommended for spot heater application.

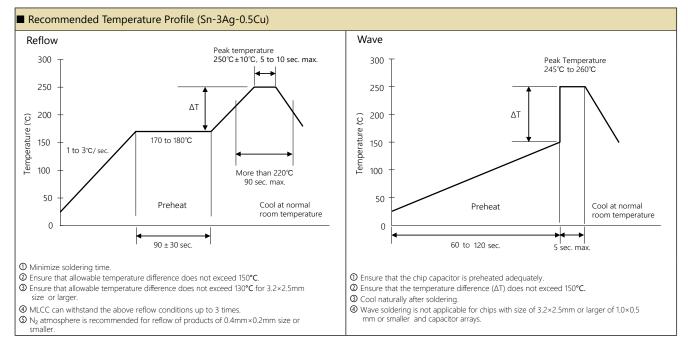
· Recommended spot heater condition

Item	Condition		
Distance	5mm min.		
Angle	45°		
Projection Temp.	400°C max.		
Flow rate	Set at the minimum		
Nozzle diameter	2φ to 4φ (Single hole type)		
Application time	10 sec. max. (1206 and smaller) 30 sec. max. (1210 and larger)		



Multilayer Ceramic Chip Capacitors Surface Mounting Information





Resin Mold

- 1) If a large amount of resin is used for molding the chip, cracks may occur due to contraction stress during curing. To avoid such cracks, use a low shrinkage resin.
- 2) The insulation resistance of the chip will degrade due to moisture absorption. Use a low moisture absorption resin.
- 3) Check carefully that the resin does not generate a decomposition gas or reaction gas during the curing process or during normal storage. Such gases may crack the chip capacitor or damage the device itself.

Multilayer Ceramic Chip Capacitors Precautions



Circuit Design

- 1. Once application and assembly environments have been checked, the capacitor may be used in conformance with the rating and performance which are provided in both the catalog and the specifications. Use exceeding that which is specified may result in inferior performance or cause a short, open, smoking, or flaming to occur, etc.
- 2. Please consult the manufacturer in advance when the capacitor is used in devices such as: devices which deal with human life, i.e. medical devices; devices which are highly public orientated; and devices which demand a high standard of liability.
 Accident or malfunction of devices such as medical devices, space equipment and devices having to do with atomic power could generate grave consequence with respect to human lives or, possibly, a portion of the public. Capacitors used in these devices may require high reliability design different from that of general purpose capacitors.
- 3. Please use the capacitors in conformance with the operating temperature provided in both the catalog and the specifications.
 - Be especially cautious not to exceed the maximum temperature. In the situation the maximum temperature set forth in both the catalog and specifications is exceeded, the capacitor's insulation resistance may deteriorate, power may suddenly surge and short-circuit may occur.
 - The capacitor has a loss, and may self-heat due to equivalent series resistance when alternating electric current is passed therethrough. As this effect becomes especially pronounced in high frequency circuits, please exercise caution.
 - When using the capacitor in a (self-heating) circuit, please make sure the surface of the capacitor remains under the maximum temperature for usage. Also, please make certain temperature rises remain below 20°C.
- 4. Please keep voltage under the rated voltage which is applied to the capacitor. Also, please make certain the peak voltage remains below the rated voltage when AC voltage is super-imposed to the DC voltage. In the situation where AC or pulse voltage is employed, ensure average peak voltage does not exceed the rated voltage.
 Exceeding the rated voltage provided in both catalog and specifications may lead to defective withstanding voltage or, in worst case situations, may cause the capacitor to smoke or flame.
- 5. When the capacitor is to be employed in a circuit in which there is continuous application of a high frequency voltage or a steep pulse voltage, even though it is within the rated voltage, please inquire to the manufacturer. In the situation the capacitor is to be employed using a high frequency AC voltage or a extremely fast rising pulse voltage, even though it is within the rated voltage, it is possible capacitor reliability will deteriorate.
- 6. It is a common phenomenon of high-dielectric products to have a deteriorated amount of static electricity due to the application of DC voltage.

 Due caution is necessary as the degree of deterioration varies depending on the quality of capacitor materials, capacity, as well as the load voltage at the time of operation.
- 7. Do not use the capacitor in an environment where it might easily exceed the respective provisions concerning shock and vibration specified in the catalog and specifications. In addition, it is a common piezo phenomenon of high dielectric products to have some voltage due to vibration or to have noise due to voltage change. Please contact sales in such case.
- 8. If the electrostatic capacity value of the delivered capacitor is within the specified tolerance, please consider this when designing the respective product in order that the assembled product function appropriately.
- 9. Please contact us upon using conductive adhesives.

Storage

Please note the following regarding the storage of delivered products.

- 1. Set the storage temperature to + 5 to + 40 °C and humidity to 20 ~ 70% RH. Other meteorological conditions are in accordance with classification 1 K2 of JIS C 60721 -3 -1.
- 2. Store in a place where corrosive gas (H₂S, SO₂, NO₂, Cl₂, etc.) does not exist in the atmosphere. Also, avoid exposure to salty moisture. In either case, this may cause oxidation corrosion of the terminal electrode, reducing solderability.

If you store the above delivered products according to the conditions listed above, it will satisfy the solderability standard for 6 months from the shipping date.

Safety application guideline and detailed information of electrical properties are also provided in kyocera web site;

URL: https://ele.kyocera.com/en/product/capacitor/



Multilayer Ceramic Chip Capacitors Notes for Using the Catalog



- 1. Contents described herein are as of April 2025.
- 2. Contents in this catalog are subject to change without notice. It is recommended to confirm the latest information at the time of usage. We may not be able to accept requests based on old catalogs.
- 3. The products described in this catalog are intended for use in general electronic equipment (Information equipment, communication equipment, audio and video equipment, measurement equipment, home appliances, automotive equipment, etc.). If you plan to use the product in any equipment or system that requires special quality and reliability of those beyond catalog spec and whose failure or malfunction may directly threaten human life or cause physical harm (Safety equipment, aerospace, nuclear power control, medical equipment including life support equipment, etc.), please contact our company sales representative before using the product.
- 4. Even though we strive for improvements of quality and reliability of products, it is requested to design with enough safety margin in equipment or systems in order not to threaten human lives directly or damage human bodies or properties by an accidental result of products.
- 5. It is requested to design based on guaranteed specifications for such as maximum ratings, operating voltage and operating temperature. It is not the scope of our guarantee for unsatisfactory results due to misuse or inadequate usage of products in this catalog.
- 6. Operation summaries and circuit examples in this catalog are intended to explain typical operation and usage of the product. It is recommended to perform circuit and assembly design considering surrounding conditions upon using products in this catalog.
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